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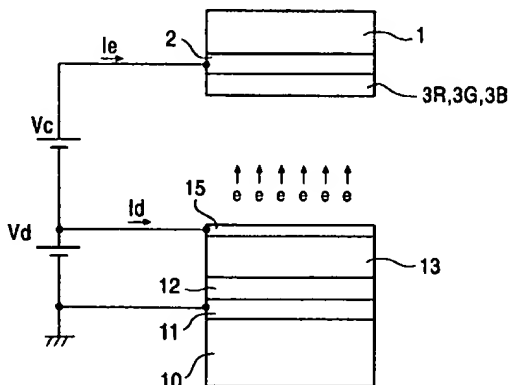
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(54) Electron emission device and display device using the same

(57) An electron emission device exhibits a high electron emission efficiency. The device includes an electron supply layer (12) of metal or semiconductor, an insulator layer (13) formed on the electron supply layer (12), and a thin-film metal electrode (15) formed on the insulator layer (13). The insulator layer (13) is made of a dielectric substance and has a film thickness of 50 nm or greater. When an electric field is applied between the electron supply layer (12) and the thin-film metal electrode (15), the electron emission device emits electrons.

FIG. 1



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12, 16, 17, 18, 19  
see Fig 26 & 34  
15  
14  
13  
15 (14)  
13

**Description****BACKGROUND OF THE INVENTION**

## 1. Field of the invention

The present invention relates to an electron emission device and an electron emission display device using the same.

## 2. Description of the related art

An FED (Field Emission Display) in field electron emission display apparatuses is known as a planar emission display equipped with an array of cold-cathode electron emission source which does not require cathode heating. For example, the emission principle of an FED using a spindt type cold cathode is in that electrons are drawn out from the cold cathode into a vacuum by the gate electrode located apart from the cold cathode in a similar manner of a CRT (Cathode Ray Tube) which has a cathode different from that of the FED, and then the electrons impinge onto the fluorescent substance that is coated on the transparent anode, thereby causing light emission.

However, this field emission source faces a problem of low production yield because the manufacture of the minute spindt type cold cathode is complex and involves many steps.

There also exists an electron emission device with a metal-insulator-metal (MIM) structure as a planar electron source. This electron emission device with an MIM structure has an aluminum (Al) layer as a cathode, an  $\text{Al}_2\text{O}_3$  insulator layer of about 10 nm in film thickness and an Au layer, as an anode, of about 10 nm in film thickness formed in order on the substrate. With this device placed under an opposing electrode in a vacuum, when a voltage is applied between the underlying Al layer and the overlying Au layer and an acceleration voltage is applied to the opposing electrode, some of electrons leap out of the overlying Au layer and reach the opposing electrode. Even the electron emission device with an MIM structure does not yet provide a sufficient amount of emitted electrons. To improve this property of emission, it is considered that there is a necessity to make the  $\text{Al}_2\text{O}_3$  insulator layer thinner by about several nanometers and make the quality of the membranous of the  $\text{Al}_2\text{O}_3$  insulator layer and the interface between the  $\text{Al}_2\text{O}_3$  insulator layer and the overlying Au layer more uniform.

To provide a thinner and more uniform insulator layer, for example, an attempt has been made to control the formation current by using anodization to thereby improve the electron emission characteristic, as in the invention described in Japanese Patent Application kokai No. Hei 7-65710.

However, even such an electron emission device with an MIM structure which is manufactured by this method still ensures a sufficient electron emission property of an emission current of about  $1 \times 10^{-5}$  A/cm<sup>2</sup> and an electron emission efficiency (emission current/diode current) of about  $1 \times 10^{-3}$ . Since there is a difficulty in the method using anodization the electron emission device is not put to practical use yet. One of the factors influencing the device property of the electron emission device with an MIM structure is the quality and thickness of the insulator layer. It is considered that there is a necessity to make the insulator layer still thinner enough for occurrence of tunnel effect at i.e., about several nanometers in thickness. Therefore, pinholes are apt to appear in the insulator layer, so that the quality of film thereof is spoiled. Further one of the factors influencing the device property of the electron emission device with the MIM structure is the state of interface between the insulator layer and the metal layer. If the surface of the insulator layer after deposition is rough then the contact between the insulator layer and the metal layer becomes poor so as to make the electron emission unstable in the device.

**SUMMARY OF THE INVENTION**

Accordingly, it is an object of the present invention to provide an electron emission device with a high electron emission efficiency and an electron emission display apparatus using the same.

An electron emission device according to the invention comprises:

an electron supply layer of metal or semiconductor;  
an insulator layer formed on the electron supply layer; and  
a thin-film metal electrode formed on the insulator layer and facing a vacuum space,  
characterized in that said insulator layer is made of a dielectric substance and has a film thickness of 50 nm or greater, whereby the electron emission device emits electrons when an electric field is applied between the electron supply layer and the thin-film metal electrode.

According to the electron emission device of the invention with the above structure, the insulator layer has a large

thickness so that any holes are not likely to be produced, whereby the production yield is improved. The emission current of the electron emission device is greater than  $1 \times 10^{-6}$  A/cm<sup>2</sup> and is approximately  $1 \times 10^{-3}$  A/cm<sup>2</sup>, and the electron emission efficiency obtained is  $1 \times 10^{-1}$ . Therefore, when this electron emission device is used in a display device, it can provide a high luminance, and suppress the consumption of the drive current to reduce the generation of heat, and can reduce a load on the driving circuit for the device.

The electron emission device of the invention is a planar or spot-like electron emission diode and can be adapted to high speed devices such as a source of a pixel vacuum tube or bulb, an electromagnetic emission source of an electron microscope, a vacuum-micro electronics device and the like. In addition, this electron emission device can serve as a light-emitting diode or a laser diode which emits electromagnetic waves of infrared rays, visible light or ultraviolet rays.

Moreover a display device using an electron emission device according to the invention comprises:

a pair of first and second substrates facing each other with a vacuum space in between;  
 a plurality of electron emission devices provided on the first substrate;  
 a collector electrode provided in the second substrate; and  
 a fluorescent layer formed on the collector electrode,  
 each of the electron emission devices comprising an electron supply layer of metal or semiconductor; an insulator layer formed on the electron supply layer; and a thin-film metal electrode formed on the insulator layer and facing a vacuum space, wherein said insulator layer is made of a dielectric substance and has a film thickness of 50 nm or greater.

In addition, a display device using an electron emission device according to the invention comprises:

a pair of a device substrate and a transparent substrate facing each other with a vacuum space in between;  
 a plurality of ohmic electrodes formed in parallel on an inner surface of the device substrate;  
 a plurality of electron emission devices provided on the ohmic electrodes, each of the electron emission devices comprising an electron supply layer of metal or semiconductor; an insulator layer formed on the electron supply layer; and a thin-film metal electrode formed on the insulator layer and facing the vacuum space, wherein said insulator layer is made of a dielectric substance and has a film thickness of 50 nm or greater;  
 a plurality of bus electrodes formed on parts of the thin-film metal electrodes and extending in parallel to one another and perpendicular to the ohmic electrodes so as to electrically connect adjoining thin-film metal electrodes;  
 a plurality of collector electrodes provided in the transparent substrate; and  
 fluorescent layers formed on the collector electrodes.

In an embodiment of the electron emission display device, the device further comprises a second auxiliary insulator layer with openings formed to separate the surfaces of the thin-film metal electrodes into a plurality of electron emission regions. The second auxiliary insulator layer covers the bus electrodes.

In an aspect of the invention, an electron emission device comprises:

an electron supply layer of metal or semiconductor;  
 an insulator layer formed on the electron supply layer; and  
 a thin-film metal electrode formed on the insulator layer and facing a vacuum space,  
 characterized in that said insulator layer is made of a dielectric substance with a dielectric constant of 8 or less and has a film thickness of 50 nm or greater, whereby the electron emission device emits electrons when an electric field is applied between the electron supply layer and the thin-film metal electrode.

In another aspect of the invention, an electron emission display device comprises:

a pair of first and second substrates facing each other with a vacuum space in between;  
 a plurality of electron emission devices provided on the first substrate;  
 a collector electrode provided in the second substrate; and  
 a fluorescent layer formed on the collector electrode,  
 each of the electron emission devices comprising an electron supply layer of metal or semiconductor; an insulator layer formed on the electron supply layer; and a thin-film metal electrode formed on the insulator layer and facing a vacuum space, wherein said insulator layer is made of a dielectric substance with a dielectric constant of 8 or less and has a film thickness of 50 nm or greater.

In another aspect of the invention, an electron emission device comprises:

an electron supply layer of metal or semiconductor;  
 an insulator layer formed on the electron supply layer; and  
 a thin-film metal electrode formed on the insulator layer and facing a vacuum space,  
 characterized in that said insulator layer has a film thickness of 50 nm or greater and said electron supply layer is  
 5 transparent, whereby the electron emission device emits electrons when an electric field is applied between the  
 electron supply layer and the thin-film metal electrode.

In a further other aspect of the invention, an electron emission display device comprises:

10 a pair of first and second substrates facing each other with a vacuum space in between;  
 a plurality of electron emission devices provided on the first substrate;  
 a collector electrode provided in the second substrate; and  
 a fluorescent layer formed on the collector electrode,  
 each of the electron emission devices comprising an electron supply layer of metal or semiconductor; an insulator  
 15 layer formed on the electron supply layer; and a thin-film metal electrode formed on the insulator layer and facing  
 a vacuum space, wherein said insulator layer has a film thickness of 50 nm or greater and said electron supply  
 layer is transparent.

In an embodiment of the electron emission display device of the invention, said first substrate is transparent, and  
 20 further comprising a transparent electrode which is disposed between said first substrate and said electron supply layer.

In another aspect of the invention, an electron emission device comprises:

an electron supply layer of metal or semiconductor;  
 an insulator layer formed on the electron supply layer; and  
 25 a thin-film metal electrode formed on the insulator layer and facing a vacuum space,  
 characterized in that said insulator layer is made of silicon oxide as a major component of  $\text{SiO}_x$  where x represents  
 an atomic ratio in a range of from 0.5 to 2 in a whole layer and has a film thickness of 50 nm or greater, whereby  
 the electron emission device emits electrons when an electric field is applied between the electron supply layer  
 and the thin-film metal electrode.

30 In a still further aspect of the invention, an electron emission device comprises:

an electron supply layer of metal or semiconductor;  
 an insulator layer formed on the electron supply layer; and  
 35 a thin-film metal electrode formed on the insulator layer and facing a vacuum space,  
 characterized in that said insulator layer is made of silicon oxide as a major component of  $\text{SiO}_x$  where x represents  
 an atomic ratio satisfying a refractive index in a range of from 1.3 to 3.0 and has a film thickness of 50 nm or  
 greater, whereby the electron emission device emits electrons when an electric field is applied between the electron  
 supply layer and the thin-film metal electrode.

40 In another aspect of the invention, an electron emission display device comprises:

a pair of first and second substrates facing each other with a vacuum space in between;  
 a plurality of electron emission devices provided on the first substrate;  
 45 a collector electrode provided in the second substrate; and  
 a fluorescent layer formed on the collector electrode,  
 each of the electron emission devices comprising an electron supply layer of metal or semiconductor; an insulator  
 layer formed on the electron supply layer; and a thin-film metal electrode formed on the insulator layer and facing  
 a vacuum space, wherein said insulator layer is made of silicon oxide as a major component of  $\text{SiO}_x$  where x  
 50 represents an atomic ratio in a range of from 0.5 to 2 in a whole layer and has a film thickness of 50 nm or greater.

In another aspect of the invention, an electron emission display device comprises:

a pair of a device substrate and a transparent substrate facing each other with a vacuum space in between;  
 55 a plurality of ohmic electrodes formed in parallel on an inner surface of the device substrate;  
 a plurality of electron emission devices provided on the ohmic electrodes, each of the electron emission devices  
 comprising an electron supply layer of metal or semiconductor; an insulator layer formed on the electron supply  
 layer; and a thin-film metal electrode formed on the insulator layer and facing the vacuum space, wherein said

insulator layer is made of silicon oxide as a major component of  $\text{SiO}_x$  where  $x$  represents an atomic ratio in a range of from 0.5 to 2 in a whole layer and has a film thickness of 50 nm or greater; a plurality of bus electrodes formed on parts of the thin-film metal electrodes and extending in parallel to one another and perpendicular to the ohmic electrodes so as to electrically connect adjoining thin-film metal electrodes; a plurality of collector electrodes provided in the transparent substrate; and fluorescent layers formed on the collector electrodes.

In another aspect of the invention, an electron emission device comprises:

an electron supply layer of metal or semiconductor;  
an insulator layer formed on the electron supply layer; and  
a thin-film metal electrode formed on the insulator layer and facing a vacuum space,  
characterized in that one of said thin-film metal electrode and said insulator layer is provided with a region containing atomic elements each having a work function which is lower than that of said thin-film metal electrode,  
whereby the electron emission device emits electrons when an electric field is applied between the electron supply layer and the thin-film metal electrode.

In an embodiment of the electron emission display of the invention, said region is an intermediate layer disposed between said thin-film metal electrode and said insulator layer.

In another embodiment of the electron emission display of the invention, said region is an electron emission layer disposed on an outer surface of said thin-film metal electrode at a side of electron emission.

In a further embodiment of the electron emission display of the invention, said region is disposed so as to be dispersed as a layer within said thin-film metal electrode.

In a still further embodiment of the electron emission display of the invention, said region is disposed so as to be dispersed as a layer within said insulator layer.

In an aspect of the invention, an electron emission display device comprises:

a pair of first and second substrates facing each other with a vacuum space in between;  
a plurality of electron emission devices provided on the first substrate;  
a collector electrode provided in the second substrate; and  
a fluorescent layer formed on the collector electrode,  
each of the electron emission devices comprising an electron supply layer of metal or semiconductor; an insulator layer formed on the electron supply layer; and a thin-film metal electrode formed on the insulator layer and facing a vacuum space, wherein one of said thin-film metal electrode and said insulator layer is provided with a region containing atomic elements each having a work function which is lower than that of said thin-film metal electrode.

In another aspect of the invention, an electron emission device comprises:

an electron supply layer of metal or semiconductor;  
an insulator layer formed on the electron supply layer; and  
a thin-film metal electrode formed on the insulator layer and facing a vacuum space,  
characterized in that said insulator layer has a film thickness of 50 nm or greater and is provided with one or more thermal conductive layers, whereby the electron emission device emits electrons when an electric field is applied between the electron supply layer and the thin-film metal electrode. An advantage of the invention is in that the thermal conductive layer in the insulator layer restricts a thermal distribution to improve stability and life of the device in use.

In a further aspect of the invention, an electron emission display device comprises:

a pair of first and second substrates facing each other with a vacuum space in between;  
a plurality of electron emission devices provided on the first substrate;  
a collector electrode provided in the second substrate; and  
a fluorescent layer formed on the collector electrode,  
each of the electron emission devices comprising an electron supply layer of metal or semiconductor; an insulator layer formed on the electron supply layer; and a thin-film metal electrode formed on the insulator layer and facing a vacuum space, wherein said insulator layer has a film thickness of 50 nm or greater and is provided with one or more thermal conductive layers.

In an aspect of the invention, an electron emission device comprises:

an electron supply layer of metal or semiconductor;  
 an insulator layer formed on the electron supply layer; and  
 5 a thin-film metal electrode formed on the insulator layer and facing a vacuum space,  
 characterized in that said insulator layer is made of a dielectric substance and has a film thickness of 50 nm or  
 greater which is formed through a sputtering by using a mixture gas essentially comprising a rare gas under a  
 sputtering condition of a gas pressure of 0.1 to 100 mTorr and a forming rate of 0.5 to 100 nm/min, whereby the  
 10 electron emission device emits electrons when an electric field is applied between the electron supply layer and  
 the thin-film metal electrode.

In another aspect of the invention, an electron emission display device comprises:

a pair of first and second substrates facing each other with a vacuum space in between;  
 15 a plurality of electron emission devices provided on the first substrate;  
 a collector electrode provided in the second substrate; and  
 a fluorescent layer formed on the collector electrode,  
 each of the electron emission devices comprising an electron supply layer of metal or semiconductor; an insulator  
 layer formed on the electron supply layer; and a thin-film metal electrode formed on the insulator layer and facing  
 20 a vacuum space, wherein said insulator layer is made of a dielectric substance and has a film thickness of 50 nm  
 or greater which is formed through a sputtering by using a mixture gas essentially comprising a rare gas under a  
 sputtering condition of a gas pressure of 0.1 to 100 mTorr and a forming rate of 0.5 to 100 nm/min.

In a further aspect of the invention, an electron emission display device comprises:

a pair of a device substrate and a transparent substrate facing each other with a vacuum space in between;  
 a plurality of ohmic electrodes formed in parallel on an inner surface of the device substrate;  
 a plurality of electron emission devices provided on the ohmic electrodes, each of the electron emission devices  
 comprising an electron supply layer of metal or semiconductor; an insulator layer formed on the electron supply  
 30 layer; and a thin-film metal electrode formed on the insulator layer and facing the vacuum space, wherein said  
 insulator layer is made of a dielectric substance and has a film thickness of 50 nm or greater which is formed  
 through a sputtering by using a mixture gas essentially comprising a rare gas under a sputtering condition of a  
 gas pressure of 0.1 to 100 mTorr and a forming rate of 0.5 to 100 nm/min;  
 a plurality of bus electrodes formed on parts of the thin-film metal electrodes and extending in parallel to one  
 35 another and perpendicular to the ohmic electrodes so as to electrically connect adjoining thin-film metal electrodes;  
 a plurality of collector electrodes provided in the transparent substrate; and  
 fluorescent layers formed on the collector electrodes.

In an aspect of the invention, an electron emission device comprises:

an electron supply layer of metal or semiconductor;  
 an insulator layer formed on the electron supply layer; and  
 a thin-film metal electrode formed on the insulator layer and facing a vacuum space,  
 40 characterized in that said insulator layer has a smoothed surface layer for contacting with said thin-film metal  
 electrode, whereby the electron emission device emits electrons when an electric field is applied between the  
 45 electron supply layer and the thin-film metal electrode.

In an embodiment of the electron emission device of the invention, said insulator layer is made of a dielectric  
 substance and has a film thickness of 50 nm or greater which is formed through a sputtering method under a sputtering  
 50 condition in which a gas pressure or a forming rate for said surface layer of said insulator layer is lower than that for  
 a portion other than said surface layer of said insulator layer.

In another embodiment of the electron emission device of the invention, said insulator layer is made of a dielectric  
 substance and has a film thickness of 50 nm or greater which is formed through a sputtering method by using a mixture  
 gas essentially comprising a rare gas in such a manner that a portion other than said surface layer of said insulator  
 55 layer is deposited under a sputtering condition of a gas pressure of 2 to 100 mTorr and a forming rate of 0.1 to 100  
 nm/min before said surface layer of said insulator layer is deposited under a sputtering condition of a gas pressure of  
 0.1 to 1 mTorr and a forming rate of 0.1 to 100 nm/min.

In a further embodiment of the electron emission device of the invention, said insulator layer is made of a dielectric

substance and has a film thickness of 50 nm or greater which is formed through a sputtering method by using a mixture gas essentially comprising a rare gas in such a manner that a portion other than said surface layer of said insulator layer is deposited under a sputtering condition of a gas pressure of 0.1 to 100 mTorr and a forming rate of 20 to 100 nm/min before said surface layer of said insulator layer is deposited under a sputtering condition of a gas pressure of 0.1 to 100 mTorr and a forming rate of 0.1 to 10 nm/min.

In a still further embodiment of the electron emission device of the invention, said mixture gas contains xenon or krypton.

In a still further embodiment of the electron emission device of the invention, an electric resistance of said surface layer for contacting with said thin-film metal electrode is higher than that of a portion other than said surface layer of said insulator layer.

In a still further embodiment of the electron emission device of the invention, an electric resistance of said surface layer for contacting with said thin-film metal electrode is smoothed by a sputter-etching.

In a still further embodiment of the electron emission device of the invention, said electron supply layer is made of silicon and said insulator is made of silicon oxide.

In another aspect of the invention, an electron emission display device comprises:

a pair of first and second substrates facing each other with a vacuum space in between;  
a plurality of electron emission devices provided on the first substrate;  
a collector electrode provided in the second substrate; and  
a fluorescent layer formed on the collector electrode,  
each of the electron emission devices comprising an electron supply layer of metal or semiconductor; an insulator layer formed on the electron supply layer; and a thin-film metal electrode formed on the insulator layer and facing a vacuum space, wherein said insulator layer has a smoothed surface layer for contacting with said thin-film metal electrode.

An advantage of the invention is in that the smoothed surface layer for contacting with the thin-film metal electrode in the insulator layer secures an electric connection therebetween for uniformly making an electric field applied to the insulator layer to improve the stableness of electron emission property of the device. The smoothing treatment is achieved by a sputtering method under a sputtering condition in which a gas pressure or a forming rate for the surface layer of the insulator layer is lower than that for a portion other than the surface layer of the insulator layer.

#### BRIEF DESCRIPTION OF THE DRAWINGS

Fig. 1 is a schematic cross-sectional view of an electron emission device of an embodiment according to the invention;

Fig. 2 is a graph illustrating a dependency of the electron emission current on the film thickness of an  $\text{SiO}_2$  layer in the electron emission device in an embodiment of the invention;

Fig. 3 is a graph showing a dependency of the electron emission efficiency on the film thickness of the  $\text{SiO}_2$  layer in the electron emission device in the embodiment of the invention;

Fig. 4 is a graph illustrating a dependency of the electron emission current on the gas pressure in the electron emission device in another embodiment of the invention;

Fig. 5 is a graph showing a dependency of the electron emission efficiency on the gas pressure in the electron emission device in another embodiment of the invention;

Fig. 6 is a graph illustrating a dependency of the electron emission current on the deposition rate in the electron emission device in another embodiment of the invention;

Fig. 7 is a graph showing a dependency of the electron emission efficiency on the deposition rate in the electron emission device in another embodiment of the invention;

Fig. 8 is an SEM photograph showing a surface of an insulator layer deposited by the sputtering method for an embodiment of the invention in which a particle structure appears;

Fig. 9 is an SEM photograph showing a surface of an insulator layer deposited by a CVD method for a comparative;

Fig. 10 is an energy band diagram illustrating a large thickness-insulator layer structure of an electron emission device according to the present invention;

Fig. 11 is a schematic cross-sectional view showing an electron emission device of an embodiment according to the invention in which a surface layer is provided with the insulator layer;

Fig. 12 is a graph illustrating a dependency of the electron emission current on the film thickness of an  $\text{SiO}_2$  insulator layer in an electron emission device embodying the invention;

Fig. 13 is a graph showing a dependency of the electron emission efficiency on the film thickness of the  $\text{SiO}_2$  insulator layer in the electron emission device embodying the invention;



Fig. 14 is a graph illustrating a relationship between the emission current  $I_e$  and the driving voltage  $V_d$  applied for driving the electron emission device of an embodiment according to the invention together with that of a comparative electron emission device;

Figs. 15A and 15B are graphs illustrating fluctuations of emission current with the lapse of time in the electron emission device of the embodiment and the comparative electron emission device respectively;

Fig. 16 is a graph illustrating a dependency of the electron emission current on the atomic ratio  $x$  of an  $\text{SiO}_x$  layer in the electron emission device according to the invention;

Fig. 17 is a graph showing a dependency of the electron emission efficiency on the atomic ratio  $x$  of the  $\text{SiO}_x$  layer in the electron emission device according to the invention;

Fig. 18 is a graph illustrating a dependency of the electron emission current on the refractive index of an  $\text{SiO}_x$  layer in the electron emission device embodying the invention;

Fig. 19 is a graph showing a dependency of the electron emission efficiency on the refractive index of the  $\text{SiO}_x$  layer in the electron emission device embodying the invention;

Fig. 20 is an energy band diagram illustrating an interface between a metal and a semiconductor of an electron emission device according to the present invention which shows a lowered Schottky barrier caused by the combined effects of the image force and the electric field applied to the interface;

Fig. 21 is a graph showing a relationship between a dielectric constant of insulator layer and the emission current in an electron emission device according to the invention;

Fig. 22 is a graph showing a relationship between a dielectric constant of insulator layer and the electron emission efficiency in the electron emission device according to the invention;

Fig. 23 is a graph illustrating a dependency of the electron emission current on the film thickness of an insulator layer in the electron emission device embodying the invention;

Fig. 24 is a graph showing a dependency of the electron emission efficiency on the film thickness of the insulator layer in the electron emission device embodying the invention;

Fig. 25 is a schematic cross-sectional view of an electron emission device of another embodiment according to the invention which emits light toward both sides;

Fig. 26 is a schematic cross-sectional view showing an electron emission device of a further embodiment according to the invention which has an intermediate layer disposed between the thin-film metal electrode and the insulator layer;

Fig. 27 is a graph illustrating a dependency of the electron emission current on the film thickness of an  $\text{SiO}_2$  layer in the electron emission device in another embodiment of the invention;

Fig. 28 is a graph showing a dependency of the electron emission efficiency on the film thickness of the  $\text{SiO}_2$  layer in the electron emission device in another embodiment of the invention;

Fig. 29 is a graph illustrating a relationship between the emission current  $I_e$  and the driving voltage  $V_d$  applied for driving the electron emission device of an embodiment according to the invention;

Fig. 30 is a partly enlarged schematic cross-sectional view showing an electron emission device of a still further embodiment according to the invention which has an electron emission layer disposed on an outer surface of the thin-film metal electrode;

Fig. 31 is a graph illustrating a dependency of the electron emission current on the film thickness of an  $\text{SiO}_2$  layer in the electron emission device shown in Fig. 30;

Fig. 32 is a graph showing a dependency of the electron emission efficiency on the film thickness of the  $\text{SiO}_2$  layer in the electron emission device shown in Fig. 30;

Fig. 33 is a graph illustrating a relationship between the emission current  $I_e$  and the driving voltage  $V_d$  applied for driving the electron emission device shown in Fig. 30;

Fig. 34 is a partly enlarged schematic cross-sectional view showing an electron emission device of another embodiment according to the invention in which the thin-film metal electrode has a region containing atomic elements each having a work function which is lower than that of the thin-film metal electrode;

Fig. 35 is a graph illustrating a dependency of the electron emission current on the film thickness of an  $\text{SiO}_2$  insulator layer in the electron emission device shown in Fig. 34;

Fig. 36 is a graph showing a dependency of the electron emission efficiency on the film thickness of the  $\text{SiO}_2$  insulator layer in the electron emission device shown in Fig. 34;

Fig. 37 is a graph illustrating a relationship between the diode current  $I_d$  for driving the device and the emission current  $I_e$  in the electron emission device shown in Fig. 34;

Fig. 38 is a graph illustrating a fluctuation of emission current with the lapse of time in the electron emission device shown in Fig. 20;

Fig. 39 is a graph illustrating a fluctuation of emission current with the lapse of time in a comparative electron emission device which has no region containing atomic elements each having a work function which is lower than that of the thin-film metal electrode;

Fig. 40 is a graph illustrating relationships of both the diode current  $I_d$  and the emission current  $I_e$  with respect to the driving voltage  $V_d$  applied for driving the electron emission device shown in Fig. 20;

Fig. 41 is an partly enlarged schematic cross-sectional view showing an electron emission device of a further embodiment according to the invention in which the insulator layer has a region containing atomic elements each having a work function which is lower than that of the thin-film metal electrode;

Fig. 42 is a schematic cross-sectional view showing an electron emission device of a still further embodiment according to the invention in which a thermal conductive layer is provided within the insulator layer;

Fig. 43 is a graph illustrating a dependency of the electron emission current on the film thickness of an  $\text{SiO}_2$  insulator layer in an electron emission device embodying the invention;

Fig. 44 is a graph showing a dependency of the electron emission efficiency on the film thickness of the  $\text{SiO}_2$  insulator layer in the electron emission device embodying the invention;

Fig. 45 is a graph showing changes in temperature of the electron emission device with the lapse of time in an embodiment according to the invention as well as an comparative electron emission device which has no thermal conductive layer;

Figs. 46A and 46B are graphs illustrating fluctuations of emission current with the lapse of time in the electron emission device of the embodiment having a thermal conductive layer provided within the insulator layer and the comparative electron emission device respectively; and

Fig. 47 is a schematic perspective view showing an electron emission display device according to one embodiment of the invention.

## DETAILED DESCRIPTION OF THE PREFERRED EMBODIMENTS

Preferred embodiments according to the present invention will be described in more detail with reference to the accompanying drawings.

### <Electron emission device with a thick insulator layer>

As shown in Fig. 1, an electron emission device embodying the invention has an ohmic electrode 11 of Al previously formed on a device substrate 10. The electron emission device has also an electron supply layer 12 of metal or semiconductor such as Si or the like, an insulator layer 13 of  $\text{SiO}_x$  such as  $\text{SiO}_2$  or the like and a thin-film metal electrode 15 of metal such as Au facing a vacuum space which are layered or formed on the ohmic electrode in the described order. A pair of the device substrate 10 of a first substrate and a transparent substrate 1 are supported so as to face each other sandwiching a vacuum space therebetween. On the internal surface of the transparent substrate 1, a transparent collector electrode 2 and a fluorescent layer 3R, 3G or 3B of fluorescent substance are formed in turn.

The insulator layer 13 is made of a dielectric substance and has a very large film thickness of 50 nm or greater. The electron emission device can be regarded as a diode of which the thin-film metal electrode 15 at its surface is connected to a positive potential  $V_d$  and the back, i.e., ohmic electrode 11 is connected to a ground potential. The electron emission device emits electrons when an electric field is applied between the electron supply layer and the thin-film metal electrode. When the driving voltage  $V_d$  is applied between the ohmic electrode 11 and the thin-film metal electrode 15 to supply electrons into the electron supply layer 12, a diode current  $I_d$  flows. Since the insulator layer 13 has a high resistance, most of the applied electric field is applied to the insulator layer 13. The electrons travel inside the insulator layer 13 toward the thin-film metal electrode 15. Some of the electrons that reach near the thin-film metal electrode 15 tunnel through the thin-film metal electrode 15, due to the strong field, to be discharged out into the vacuum space.

The electrons  $e$  (emission current  $I_e$ ) discharged from the thin-film metal electrode 15 by the tunnel effect are accelerated by a high voltage  $V_c$  which is applied to the opposing transparent collector electrode 2, and is collected at the collector electrode 2. When the fluorescent substance is coated on the collector electrode 2, corresponding visible light is emitted.

While Si is particularly effective as a material for the electron supply layer 12 of the electron emission device, an elemental semiconductor or a compound semiconductor of an element of a group IV, a group III-V, a group II-VI or the like, such as a germanium (Ge), silicon carbide (SiC), gallium arsenide (GaAs), indium phosphide (InP), or cadmium selenide (CdSe) can be used as well.

While metals such as Al, Au, Ag and Cu are effective as the electron supplying material, Sc, Ti, V, Cr, Mn, Fe, Co, Ni, Zn, Ga, Y, Zr, Nb, Mo, Tc, Ru, Rh, Pd, Cd, Ln, Sn, Ta, W, Re, Os, Ir, Pt, Tl, Pb, La, Ce, Pr, Nd, Pm, Sm, Eu, Gd, Tb, Dy, Ho, Er, Tm, Yb, Lu and the like can be used as well.

Silicon oxide  $\text{SiO}_x$  (wherein subscripted x represents an atomic ratio) is effective as the dielectric material of the insulator layer 13 and, metal oxides or metal nitrides such as  $\text{LiO}_x$ ,  $\text{LiN}_x$ ,  $\text{NaO}_x$ ,  $\text{KO}_x$ ,  $\text{RbO}_x$ ,  $\text{CsO}_x$ ,  $\text{BeO}_x$ ,  $\text{MgO}_x$ ,  $\text{MgN}_x$ ,  $\text{CaO}_x$ ,  $\text{CaN}_x$ ,  $\text{SrO}_x$ ,  $\text{BaO}_x$ ,  $\text{ScO}_x$ ,  $\text{YO}_x$ ,  $\text{YN}_x$ ,  $\text{LaO}_x$ ,  $\text{LaN}_x$ ,  $\text{CeO}_x$ ,  $\text{PrO}_x$ ,  $\text{NdO}_x$ ,  $\text{SmO}_x$ ,  $\text{EuO}_x$ ,  $\text{GdO}_x$ ,  $\text{TbO}_x$ ,  $\text{DyO}_x$ ,  $\text{HoO}_x$ ,

ErO<sub>x</sub>, TmO<sub>x</sub>, YbO<sub>x</sub>, LuO<sub>x</sub>, TiO<sub>x</sub>, TiN<sub>x</sub>, ZrO<sub>x</sub>, ZrN<sub>x</sub>, HfO<sub>x</sub>, HfN<sub>x</sub>, ThO<sub>x</sub>, VO<sub>x</sub>, VN<sub>x</sub>, NbO<sub>x</sub>, TaO<sub>x</sub>, TaN<sub>x</sub>, CrO<sub>x</sub>, CrN<sub>x</sub>, MoO<sub>x</sub>, MoN<sub>x</sub>, WO<sub>x</sub>, WN<sub>x</sub>, MnO<sub>x</sub>, ReO<sub>x</sub>, FeO<sub>x</sub>, FeN<sub>x</sub>, RuO<sub>x</sub>, OsO<sub>x</sub>, CoO<sub>x</sub>, RhO<sub>x</sub>, IrO<sub>x</sub>, NiO<sub>x</sub>, PdO<sub>x</sub>, PtO<sub>x</sub>, CuO<sub>x</sub>, CuN<sub>x</sub>, AgO<sub>x</sub>, AuO<sub>x</sub>, ZnO<sub>x</sub>, CdO<sub>x</sub>, HgO<sub>x</sub>, BO<sub>x</sub>, BN<sub>x</sub>, AlO<sub>x</sub>, AlN<sub>x</sub>, GaO<sub>x</sub>, GaN<sub>x</sub>, InO<sub>x</sub>, TiO<sub>x</sub>, TiN<sub>x</sub>, SiN<sub>x</sub>, GeO<sub>x</sub>, SnO<sub>x</sub>, PbO<sub>x</sub>, PO<sub>x</sub>, PN<sub>x</sub>, AsO<sub>x</sub>, SbO<sub>x</sub>, SeO<sub>x</sub>, TeO<sub>x</sub> and the like can be used as well. Furthermore, metal complex oxides such as LiAlO<sub>2</sub>, Li<sub>2</sub>SiO<sub>3</sub>, Li<sub>2</sub>TiO<sub>3</sub>, Na<sub>2</sub>Al<sub>22</sub>O<sub>34</sub>, NaF<sub>2</sub>O<sub>2</sub>, Na<sub>4</sub>SiO<sub>4</sub>, K<sub>2</sub>SiO<sub>3</sub>, K<sub>2</sub>TiO<sub>3</sub>, K<sub>2</sub>WO<sub>4</sub>, Rb<sub>2</sub>CrO<sub>4</sub>, Cs<sub>2</sub>CrO<sub>4</sub>, MgAl<sub>2</sub>O<sub>4</sub>, MgFe<sub>2</sub>O<sub>4</sub>, MgTiO<sub>3</sub>, CaTiO<sub>3</sub>, CaWO<sub>4</sub>, CaZrO<sub>3</sub>, SrF<sub>12</sub>O<sub>19</sub>, SrTiO<sub>3</sub>, SrZrO<sub>3</sub>, BaAl<sub>2</sub>O<sub>4</sub>, BaF<sub>12</sub>O<sub>19</sub>, BaTiO<sub>3</sub>, Y<sub>3</sub>Al<sub>5</sub>O<sub>12</sub>, Y<sub>3</sub>F<sub>5</sub>O<sub>12</sub>, LaFeO<sub>3</sub>, La<sub>3</sub>Fe<sub>5</sub>O<sub>12</sub>, La<sub>2</sub>Ti<sub>2</sub>O<sub>7</sub>, CeSnO<sub>4</sub>, CeTiO<sub>4</sub>, Sm<sub>3</sub>Fe<sub>5</sub>O<sub>12</sub>, EuFeO<sub>3</sub>, Eu<sub>3</sub>Fe<sub>5</sub>O<sub>12</sub>, GdFeO<sub>3</sub>, Gd<sub>3</sub>Fe<sub>5</sub>O<sub>12</sub>, DyFeO<sub>3</sub>, Dy<sub>3</sub>Fe<sub>5</sub>O<sub>12</sub>, HoFeO<sub>3</sub>, Ho<sub>3</sub>Fe<sub>5</sub>O<sub>12</sub>, ErFeO<sub>3</sub>, Er<sub>3</sub>Fe<sub>5</sub>O<sub>12</sub>, Tm<sub>3</sub>Fe<sub>5</sub>O<sub>12</sub>, LuFeO<sub>3</sub>, Lu<sub>3</sub>Fe<sub>5</sub>O<sub>12</sub>, NiTiO<sub>3</sub>, Al<sub>2</sub>TiO<sub>3</sub>, FeTiO<sub>3</sub>, BaZrO<sub>3</sub>, LiZrO<sub>3</sub>, MgZrO<sub>3</sub>, HfTiO<sub>4</sub>, NH<sub>4</sub>VO<sub>3</sub>, AgVO<sub>3</sub>, LiVO<sub>3</sub>, BaNb<sub>2</sub>O<sub>6</sub>, NaNbO<sub>3</sub>, SrNb<sub>2</sub>O<sub>6</sub>, KTaO<sub>3</sub>, NaTaO<sub>3</sub>, SrTa<sub>2</sub>O<sub>6</sub>, CuCr<sub>2</sub>O<sub>4</sub>, Ag<sub>2</sub>CrO<sub>4</sub>, BaCrO<sub>4</sub>, K<sub>2</sub>MoO<sub>4</sub>, Na<sub>2</sub>MoO<sub>4</sub>, NiMoO<sub>4</sub>, BaWO<sub>4</sub>, Na<sub>2</sub>WO<sub>4</sub>, SrWO<sub>4</sub>, MnCr<sub>2</sub>O<sub>4</sub>, MnFe<sub>2</sub>O<sub>4</sub>, MnTiO<sub>3</sub>, MnWO<sub>4</sub>, CoFe<sub>2</sub>O<sub>4</sub>, ZnFe<sub>2</sub>O<sub>4</sub>, FeWO<sub>4</sub>, CoMoO<sub>4</sub>, CoTiO<sub>3</sub>, CoWO<sub>4</sub>, NiFe<sub>2</sub>O<sub>4</sub>, NiWO<sub>4</sub>, CuFe<sub>2</sub>O<sub>4</sub>, CuMoO<sub>4</sub>, CuTiO<sub>3</sub>, CuWO<sub>4</sub>, Ag<sub>2</sub>MoO<sub>4</sub>, Ag<sub>2</sub>WO<sub>4</sub>, ZnAl<sub>2</sub>O<sub>4</sub>, ZnMoO<sub>4</sub>, ZnWO<sub>4</sub>, CdSnO<sub>3</sub>, CdTiO<sub>3</sub>, CdMoO<sub>4</sub>, CdWO<sub>4</sub>, NaAlO<sub>2</sub>, MgAl<sub>2</sub>O<sub>4</sub>, SrAl<sub>2</sub>O<sub>4</sub>, Gd<sub>3</sub>Ga<sub>5</sub>O<sub>12</sub>, InFeO<sub>3</sub>, MgIn<sub>2</sub>O<sub>4</sub>, Al<sub>2</sub>TiO<sub>5</sub>, FeTiO<sub>3</sub>, MgTiO<sub>3</sub>, NaSiO<sub>3</sub>, CaSiO<sub>3</sub>, ZrSiO<sub>4</sub>, K<sub>2</sub>GeO<sub>3</sub>, Li<sub>2</sub>GeO<sub>3</sub>, Na<sub>2</sub>GeO<sub>3</sub>, Bi<sub>2</sub>Sn<sub>3</sub>O<sub>9</sub>, MgSnO<sub>3</sub>, SrSnO<sub>3</sub>, PbSiO<sub>3</sub>, PbMoO<sub>4</sub>, PbTiO<sub>3</sub>, SnO<sub>2</sub>-Sb<sub>2</sub>O<sub>3</sub>, CuSeO<sub>4</sub>, Na<sub>2</sub>SeO<sub>3</sub>, ZnSeO<sub>3</sub>, K<sub>2</sub>TeO<sub>3</sub>, K<sub>2</sub>TeO<sub>4</sub>, Na<sub>2</sub>TeO<sub>3</sub>, Na<sub>2</sub>TeO<sub>4</sub> and the like can be used as well and still furthermore, sulfides such as FeS, Al<sub>2</sub>S<sub>3</sub>, MgS, ZnS and the like, fluorides such as LiF, MgF<sub>2</sub>, SmF<sub>3</sub> and the like, chlorides such as HgCl, FeCl<sub>2</sub>, CrCl<sub>3</sub> and the like, bromides such as AgBr, CuBr, MnBr<sub>2</sub> and the like, iodide such as PbI<sub>2</sub>, CuI, FeI<sub>2</sub> and the like and metal oxidized nitrides such as SiAlON and the like can be used as well for the insulator layer.

Moreover, carbon such as diamond, Fulleren (C<sub>2n</sub>) and the like or metal carbide such as Al<sub>4</sub>C<sub>3</sub>, B<sub>4</sub>C, CaC<sub>2</sub>, Cr<sub>3</sub>C<sub>2</sub>, Mo<sub>2</sub>C, MoC, NbC, SiC, TaC, TiC, VC, W<sub>2</sub>C, WC, ZrC and the like are also effective as the dielectric material of the insulator layer 13. Fulleren (C<sub>2n</sub>) consists of carbon atoms. The representative C<sub>60</sub> is a spherical surface basket molecule as known a soccer ball molecule. There is also known C<sub>32</sub> to C<sub>960</sub> and the like. The subscribed x in O<sub>x</sub>, N<sub>x</sub> and the like in the above chemical formulas represent atomic ratios and also herein after.

The film thickness of the insulator layer 13 may be 50 nm or greater preferably 100 to 1000 nm and more preferably 100 to 700 nm.

Metals Pt, Au, W, Ru and Ir are effective as the material for the thin-film metal electrode 15 on the electron emission side. In addition, Al, Sc, Ti, V, Cr, Mn, Fe, Co, Ni, Cu, Zn, Ga, Y, Zr, Nb, Mo, Tc, Rh, Pd, Ag, Cd, Ln, Sn, Ta, Re, Os, Tl, Pb, La, Ce, Pr, Nd, Pm, Sm, Eu, Gd, Tb, Dy, Ho, Er, Tm, Yb, Lu and the like can be used as well for the thin-film metal electrode.

The material for the device substrate 10 may be ceramics such as Al<sub>2</sub>O<sub>3</sub>, Si<sub>3</sub>N<sub>4</sub> or BN instead of glass.

Although sputtering is particularly effective in forming those layers and the substrate, vacuum deposition, CVD (Chemical Vapor Deposition), laser ablation, MBE (Molecular Beam Epitaxy) and ion beam sputtering are also effective.

Electron emission devices according to the invention were fabricated and their characteristics were examined concretely.

An electron supply layer of silicon (Si) was formed at 5000 nm thick, by sputtering, on an electrode surface of a device substrate 10 of glass on which an ohmic electrode of Al was previously formed 300 nm thick by sputtering. A plurality of Si substrates of this type were prepared.

Then, insulator layers of SiO<sub>2</sub> were formed on the electron supply layers of the Si substrates by sputtering respectively while changing the film thickness thereof in a range from 0 nm to 500 nm. Thus a plurality of SiO<sub>2</sub>-insulator substrates were provided. Each SiO<sub>2</sub> insulator layer was formed by sputtering by using a gas of Ar, Kr or Xe or a mixture thereof, or a gas mixture essentially consisting of one of those rare gases with O<sub>2</sub>, N<sub>2</sub>, H<sub>2</sub> or the like mixed therein, under the sputtering conditions of a gas pressure of 0.1 to 100 mTorr, preferably 0.4 to 40 mTorr and the forming rate of 0.1 to 1000 nm/min, preferably 0.5 to 100 nm/min. The used target was made of SiO<sub>2</sub>. The material such as SiO, Si or a composite thereof may be used for the target of sputtering. An RF supply may be used for a non-conductive target, and the RF or DC supply may be used for a conductive target. The sputtering device was controlled by properly altering the sputtering target and sputtering conditions in order to obtain a single layer or multilayer structure, the amorphous or crystal phase, the grain size and the atomic ratio of the goal insulator layer 13.

The analysis on the SiO<sub>2</sub> insulator layer in this embodiment by using an X-ray diffraction was performed and then the result showed some diffraction intensity Ic caused by the crystal portion and some halo intensity Ia caused by the amorphous phase. It can be assumed from this result that SiO<sub>2</sub> of the insulator layer consists of a dispersed polycrystal portion and an amorphous phase portion.

Finally, a thin-film metal electrode of Pt was formed at a 10 nm thick on the surface of the amorphous SiO<sub>2</sub> layer of each substrate by sputtering, thus providing plural device substrates.

Meanwhile, transparent substrates were prepared, each of which has an ITO collector electrode formed inside a transparent glass substrate and has a fluorescent layer of a fluorescent substance corresponding to R, G or B color emission formed on the collector electrode by the normal scheme.

Electron emission devices were assembled in each of which the device substrate and the transparent substrate

are supported apart from one another by 10 mm in parallel by a spacer in such a way that the thin-film metal electrode 15 faced the collector electrode 2, with the clearance therebetween made to a vacuum of  $10^{-7}$  Torr or  $10^{-5}$  Pa.

Then, the diode current  $I_d$  and the emission current  $I_e$  corresponding to the thickness of the  $\text{SiO}_2$  film of each of the acquired plural devices were measured.

Figs. 2 and 3 show the relationships between the film thickness of each  $\text{SiO}_2$  layer and the maximum emission current  $I_e$ , and between the film thickness and the maximum electron emission efficiency ( $I_e/I_d$ ) for each film thickness respectively when a driving voltage  $V_d$  of 0 to 200 V was applied to the prepared electron emission devices. As apparent from Figs. 2 and 3, while the emission current and the electron emission efficiency were saturated from the thickness of 50 nm, the devices whose  $\text{SiO}_2$  layers had thicknesses of 300 to 400 nm showed the maximum emission current of about  $1 \times 10^{-3}$  A/cm<sup>2</sup> and the maximum electron emission efficiency of about  $1 \times 10^{-1}$ .

It is understood from those results that by applying a voltage of 200 V or lower, the emission current of  $1 \times 10^{-6}$  A/cm<sup>2</sup> or greater and the electron emission efficiency of  $1 \times 10^{-3}$  or greater can be acquired from an electron emission device which has an  $\text{SiO}_2$  dielectric layer 50 nm or greater in thickness, preferably 100 to 400 nm in thickness.

With a voltage of approximately 4 kV applied between the fluorescent-substance coated collector electrode and the thin-film metal electrode, a uniform fluorescent pattern corresponding to the shape of the thin-film metal electrode was observed in the devices whose  $\text{SiO}_2$  layers have thicknesses of 50 nm or greater. This shows that the electron emission from the amorphous  $\text{SiO}_2$  layer is uniform and has a high linearity, and that those devices can serve as an electron emission diode, or a light-emitting diode or laser diode which emits electromagnetic waves of infrared rays, visible light or ultraviolet rays.

Next, various electron emission devices were manufactured through the same manner as the above embodiments except that the insulator layers each having a 400 nm thickness were formed while controlling the sputter conditions of the mixture gas pressure or the forming rate i.e., deposition rate of  $\text{SiO}_2$  respectively. There were measured the diode current  $I_d$  and the emission current  $I_e$  corresponding to the mixture gas pressure or the forming rate i.e., deposition rate of  $\text{SiO}_2$ .

Figs. 4 and 5 show the relationships between the gas pressure for deposition of  $\text{SiO}_2$  and both the maximum emission current  $I_e$  and the maximum electron emission efficiency ( $I_e/I_d$ ) respectively. As seen from these Figures, it is understood that the emission current of  $1 \times 10^{-6}$  A/cm<sup>2</sup> or greater and the electron emission efficiency of  $1 \times 10^{-3}$  or greater can be acquired from an electron emission device which has an  $\text{SiO}_2$  insulator layer formed at a gas pressure of 0.1 to 100 mTorr.

Figs. 6 and 7 show the relationships between the deposition rate of  $\text{SiO}_2$  layer and both the maximum emission current  $I_e$  and the maximum electron emission efficiency ( $I_e/I_d$ ) respectively. As seen from these Figures, it is understood that the emission current of  $1 \times 10^{-6}$  A/cm<sup>2</sup> or greater and the electron emission efficiency of  $1 \times 10^{-3}$  or greater can be acquired from an electron emission device which has an  $\text{SiO}_2$  insulator layer formed at a deposition rate of 0.5 to 100 nm/min.

When there were observations of the surface of the  $\text{SiO}_2$  insulator layer by a scanning electron microscope (SEM) during the above formation process, grain surface each having an about 20 nm diameter appeared in comparison with that formed by CVD. Figs. 8 and 9 are SEM photographs showing surfaces of insulator layers deposited by the sputtering method and the CVD method respectively.

The peculiar phenomenon that the tunnel current flows through the insulator layer which has a thickness of 50 nm or greater seems to be originated from the grain structure of  $\text{SiO}_2$  of the insulator layer. As shown in Fig. 10, while  $\text{SiO}_2$  is an insulator by nature, multiple bands with low potentials are caused by the grain structure defects adjacent thereto or impurities in the insulator layer. It is assumed that electrons tunnel through one low-potential band after another, and thus tunnel through the insulator layer of 50 nm or greater in thickness as a consequence.

#### <Smoothing of an insulator layer>

As shown in Fig. 11, an electron emission device of this embodiment has an electron supply layer 12 of metal or semiconductor, an insulator layer 13, a surface layer 14 and a thin-film metal electrode 15 facing a vacuum space which are layered or formed in turn on an ohmic electrode 11 previously formed on a device substrate 10. The electron emission device emits electrons from the thin-film metal electrode 15 to the vacuum space when an electric field is applied between the electron supply layer 12 and the thin-film metal electrode 15. The surface layer 14 contacting with the thin-film metal electrode 15 has an about 5 nm thick and is smoothed. Concretely, the smoothing of the surface layer 14 included in the insulator layer 13 is achieved through a sputtering method under a sputtering condition in which a gas pressure or a forming rate for the surface layer of the insulator layer is lower than that for a portion other than the surface layer 14 in the insulator layer, as mentioned in detail later. The smoothed surface layer 14 is in a high density and has an electric resistance higher than that of the portion other than the surface layer 14 of the insulator layer.

Next, various electron emission devices were manufactured through the same manner as the above embodiments except the  $\text{SiO}_2$  insulator layer 13 with the surface layer 14. The after steps of forming an electron supply layer was

performed per one substrate as follows:

The SiO<sub>2</sub> insulator layer was formed on an electron supply layer by sputtering by using a gas of Ar, Kr or Xe or a mixture thereof, or a gas mixture essentially consisting of one of those rare gases with O<sub>2</sub>, N<sub>2</sub>, H<sub>2</sub> or the like mixed therein. Every SiO<sub>2</sub> insulator layer was formed in such a way that SiO<sub>2</sub> was firstly deposited on the electron supply layer under sputtering conditions of a gas pressure of 2 to 100 mTorr and a forming rate of 0.1 to 100 nm/min preferably 0.5 to 100 nm/min until a predetermined thickness and was subsequently deposited as a surface layer at about 5 nm under sputtering conditions of a gas pressure of 0.1 to 1 mTorr and at the same forming rate. We observed the deposited surface of the SiO<sub>2</sub> insulator layer was with a scanning electron microscope (SEM), and then recognized the surface being as more smoothed as before. We measured electric resistance values both of the deposited SiO<sub>2</sub> surface layer and a portion other than the surface layer of the insulator layer, so that the former was  $1 \times 10^{15} \Omega\text{cm}$  and the latter was  $1 \times 10^{13} \Omega\text{cm}$ . It was understood that the electric resistance of the surface layer for contacting with the thin-film metal electrode is higher than that of the portion other than the surface layer of the insulator.

In addition, the high electric resistance of the surface layer is achieved by changing the deposition rate of SiO<sub>2</sub> instead of the gas pressure. Namely, the SiO<sub>2</sub> insulator layer may be deposited by the sputtering using a gas of Ar, Kr or Xe or a mixture thereof, or a gas mixture essentially consisting of one of those rare gases with O<sub>2</sub>, N<sub>2</sub>, H<sub>2</sub> or the like mixed therein under a first sputtering condition of a gas pressure of 0.1 to 100 mTorr and a forming rate of 20 to 100 nm/min for the portion other than the surface layer and a second sputtering condition of the same gas pressure and a forming rate of 0.1 to 10 nm/min for the SiO<sub>2</sub> surface layer with about 5 nm of the insulator layer.

The use of Ar for the sputter gas carries out the smoothing of the SiO<sub>2</sub> surface layer of the insulator layer in the sputtering process. Further, the use of mixture gas of rare gas containing partly or wholly xenon (Xe) or krypton (Kr) contributes the more fine smoothing of the surface layer.

In addition to the two-step sputtering above mentioned, the smoothing of the surface layer may be achieved by a sputter etching i.e., inverse sputtering so that, after depositing the SiO<sub>2</sub> insulator layer, the surface thereof is etched out by the inverse sputtering to form a smoothed surface on the insulator layer. The sputter etching may be performed by using a gas of Ar, Kr or Xe under sputtering condition of a gas pressure of 1 to 100 mTorr and an electric power of 50 to 1000 W in a time period of 1 to 60 minutes.

Finally, a thin-film metal electrode of Pt was formed at a 10 nm thick on the surface of the amorphous SiO<sub>2</sub> layer of each substrate by sputtering, thus providing plural device substrates.

Meanwhile, transparent substrates were prepared in such a manner that an ITO collector electrode was formed inside the transparent glass substrate and then a fluorescent layer was formed on the collector electrode by a normal scheme respectively.

Electron emission devices were assembled in each of which the device substrate and the transparent substrate are supported apart from one another by 10 mm in parallel by a spacer in such a way that the thin-film metal electrode 15 faced the collector electrode 2, with the clearance therebetween made to a vacuum of  $10^{-7}$  Torr or  $10^{-5}$  Pa.

Then, the diode current  $I_d$  and the emission current  $I_e$  corresponding to the thickness of the SiO<sub>2</sub> film (insulator layer) of each of the acquired plural devices were measured.

Figs. 12 and 13 show the relationships between the film thickness of each SiO<sub>2</sub> layer and the maximum emission current  $I_e$ , and between the film thickness and the maximum electron emission efficiency ( $I_e/I_d$ ) for each film thickness respectively when a driving voltage  $V_d$  of 0 to 200 V was applied across the thin-film metal electrode and ohmic electrode of a ground potential in each prepared electron emission device. As apparent from Figs. 12 and 13, while the emission current and the electron emission efficiency were saturated from the thickness of 50 nm, the devices whose SiO<sub>2</sub> layers had thicknesses of 300 to 500 nm showed the maximum emission current of about  $1 \times 10^{-5} \text{ A/cm}^2$  and the maximum electron emission efficiency of about  $1 \times 10^{-1}$ .

It is understood from those results that by applying a voltage of 200 V or lower, the emission current of  $1 \times 10^{-6} \text{ A/cm}^2$  or greater and the electron emission efficiency of  $1 \times 10^{-3}$  or greater can be acquired from an electron emission device which has an SiO<sub>2</sub> dielectric layer 50 nm or greater in thickness, preferably 100 to 500 nm in thickness.

Next, comparative electron emission devices were manufactured through the same manner as the above embodiments except the SiO<sub>2</sub> insulator layer 13 without smoothing. The emission currents  $I_e$  of the electron emission devices both of the embodiment and the comparative are measured.

Fig. 14 shows a property relationship between the emission current  $I_e$  and the driving voltage  $V_d$  of the electron emission devices both of the embodiment and the comparative electron emission when a driving voltage  $V_d$  of 0 to 100 V was applied across the thin-film metal electrode and ohmic electrode of a ground potential in each prepared electron emission device. In Figure, the plots A denote a property of emission current of the device having the smoothed surface layer in the insulator layer and the plots B denote that of non-smoothed one. As seen from Fig. 14, the device having the smoothed insulator layer began to emit electrons at a driving voltage  $V_d$  of about 50 V and reached to an emission current  $I_e$  of  $2 \times 10^{-4} \text{ A/cm}^2$  at a driving voltage  $V_d$  of about 70 V. In contrast, the device without any smoothed insulator layer began to emit electrons at a driving voltage  $V_d$  of about 70 V and reached to an emission current  $I_e$  of  $2 \times 10^{-4} \text{ A/cm}^2$  at a driving voltage  $V_d$  of about 90 V. In this way, the smoothing of the insulator layer in the electron

emission device provided to reduce the driving voltage for the device.

Moreover, fluctuations of the emission current  $I_e$  of the electron emission device both of the embodiment and the comparative are measured. Fig. 15A shows the fluctuation of emission current with the laps of time in the electron emission device having a smoothed insulator layer and Fig. 15B shows the same of the comparative electron emission device having no smoothed insulator layer. As apparent from these Figures, the smoothing of the insulator layer in the electron emission device restricted the fluctuation of emission current to make the emission current  $I_e$  of the electron emission device stable.

#### <Oxygen atomic ratio of Insulator layer>

Next, various electron emission devices were manufactured through the same manner as the above embodiment except that the atomic ratio  $x$  of oxygen (O) in  $\text{SiO}_x$  of the insulator layers each having a 400 nm thickness is changed by controlling the sputter conditions. There were measured the diode current  $I_d$  and the emission current  $I_e$  corresponding to the oxygen atomic ratio  $x$  of  $\text{SiO}_x$ .

Figs. 16 and 17 show the relationships between the oxygen atomic ratio  $x$  of  $\text{SiO}_x$  layer and both the maximum emission current  $I_e$  and the maximum electron emission efficiency ( $I_e/I_d$ ) respectively. As seen from these Figures, it is understood that the emission current of  $1 \times 10^{-6} \text{ A/cm}^2$  or greater and the electron emission efficiency of  $1 \times 10^{-3}$  or greater can be acquired from an electron emission device which has an insulator layer of amorphous  $\text{SiO}_x$  having an oxygen atomic ratio  $x$  of 0.5 to 2.

#### <Refractive index of Insulator layer>

Furthermore, the foregoing electron emission devices were measured the diode current  $I_d$  and the emission current  $I_e$  corresponding to the refractive index of the insulator layer.

Figs. 18 and 19 show the relationships between the refractive index of  $\text{SiO}_2$  layer and both the maximum emission current  $I_e$  and the maximum electron emission efficiency ( $I_e/I_d$ ) respectively. As seen from these Figures, it is understood that the emission current of  $1 \times 10^{-6} \text{ A/cm}^2$  or greater and the electron emission efficiency of  $1 \times 10^{-3}$  or greater can be acquired from an electron emission device which has an insulator layer of  $\text{SiO}_2$  having a refractive index of 1.3 to 3.0. It is understood that the insulator layer is made of silicon oxide as a major component of  $\text{SiO}_x$  where  $x$  represents an atomic ratio satisfying a refractive index in a range of from 1.3 to 3.0.

#### <Dielectric constant of Insulator layer>

The junction interface between metal and semiconductor in the electron emission device is similar to the Schottky diode. We take a notice of dielectric constant and consider the potential barrier between the electron supply layer 12 and the insulator layer 13 in the MIM structure of the device comprising the electron supply layer 12, the insulator layer 13 and the thin-film metal electrode 15.

As shown in Fig. 20 there is a potential barrier in the interface of metal/insulator (semiconductor) in which carriers travel so as to match the Fermi level at an initial state.

The Schottky barrier  $\Phi_B$  has a height from the Fermi level to the conduction band of the insulator (semiconductor) in the junction interface. When an electron (charged  $Q$ ) travels from metal of the electron supply layer to the insulator layer through the interface of metal/insulator, the potential energy of an electron at a distance  $X$  from the metal surface is an image potential energy  $A$  plotted in broken line shown in Fig. 20. When an external electric field  $E$  plotted in broken line shown in the Figure is applied, the sum of the image potential energy and the external electric field is equal to the total potential energy. The Schottky barrier lowering  $\Delta \Phi_B$  therefore occurs from Schottky barrier  $\Phi_B$  at the location  $X_{\min}$  as shown in curve  $C$  in Fig. 20. When the high electric field is applied to the interface of metal/insulator, the work function lowering  $\Delta \Phi_B$  is estimated by the following formula.

$$\Delta \Phi_B = (qE/4\pi\epsilon_0\epsilon_s)^{1/2}$$

where  $E$  denotes an intensity of an applied electric field,  $q$  denotes a charge quantity,  $\epsilon_s$  denotes a dielectric constant of the insulator, and  $\epsilon_0$  denotes the dielectric constant of vacuum space.

In other words, it is understood that, as the higher applied electric field is and the lower dielectric constant of the insulator is, the height of barrier becomes lower (i.e.,  $\Delta \Phi_B$  becomes larger) so as to facilitate the injection (tunneling) of electrons from metal to semiconductor. Therefore, the selection of the insulator material having a low dielectric constant is effective to lower the height of barrier. The following Table 1 shows various materials, their dielectric constants  $\epsilon_s$  and their electric resistance values respectively.

Table 1

	Dielectric constant	Electric resistance ( $\Omega\text{cm}$ )
Si	11.9	$2.3 \times 10^5$
SiO	6	
SiO <sub>2</sub> (crystal)	5.8	$>10^{14}$
SiO <sub>2</sub> (amorphous)	3.9	$10^4 \sim 10^{16}$
Al <sub>2</sub> O <sub>3</sub> (normal)	9.2	$>10^{14}$
Al <sub>2</sub> O <sub>3</sub> (high purity alumina)	8.6~10.55	
BeO	6.4	$>10^{14}$
ZnS	5.2	
ZnO	9.0	
CaF <sub>2</sub>	6.76	
Si <sub>3</sub> N <sub>4</sub> (amorphous)	7.5	$>10^{14}$
SiC	10.0	
Ta <sub>2</sub> O <sub>3</sub>	8	
ZrO <sub>2</sub>	28	
TiO <sub>2</sub>	40	
Steatite (MgO-SiO <sub>2</sub> )	5.9	

Electron emission devices according to the invention were fabricated by using SiO<sub>2</sub>, SiO, Al<sub>2</sub>O<sub>3</sub>, SiC, Ta<sub>2</sub>O<sub>3</sub> for the insulator and their characteristics were examined specifically.

Amorphous SiO<sub>x</sub>, fusion silica (from Corning incorporated 7940 :  $\epsilon_s=3.78$ ), and Pyrex™ (from Corning incorporated 7740 :  $\epsilon_s=4.6$ ) were used as SiO<sub>x</sub>. The fusion silica has the same dielectric constant as quartz.

An electron supply layer 12 of silicon (Si) was formed at 5000 nm thick, by sputtering, on an electrode surface of a device substrate 10 of glass on which an ohmic electrode 11 of tungsten (W) was previously formed 300 nm thick by sputtering. Various substrates with the Si supply layer of this type were prepared.

Then, various insulator layers 13 above mentioned were formed on the Si electron supply layers 12 of the substrates by sputtering while changing the film thickness of the insulator layer in a range from 50 nm to 1000 nm. Thus different substrates with the insulator layer were provided. Each insulator layer was formed by sputtering by using a gas of Ar, Kr or Xe or a mixture thereof, or a gas mixture essentially consisting of one of those rare gases with O<sub>2</sub>, N<sub>2</sub> or the like mixed therein, under the sputtering conditions of a gas pressure of 0.1 to 100 mTorr, preferably 0.1 to 20 mTorr and the forming rate i.e., deposition rate of 0.1 to 1000 nm/min, preferably 0.5 to 100 nm/min. The single layer or multilayer structure, the amorphous or crystal phase, the grain size and the atomic ratio of each insulator layer 13 was able to be controlled by properly altering the sputtering target and sputtering conditions of the sputtering device.

The analysis on an SiO<sub>2</sub> insulator layer 13 in this embodiment by X-ray diffraction was performed and then the result showed some halo intensity la caused by the amorphous phase. It can be assumed from this result that SiO<sub>2</sub> of the insulator layer consists of an amorphous phase portion.

Finally, a thin-film metal electrode 15 of Pt was formed at a 10 nm thick on the surface of the insulator layer 13 of each substrate by sputtering, thus providing plural device substrates.

Then, transparent substrates 1 were prepared, each of which has an ITO collector electrode 2 formed on the inside thereof. Fluorescent layers 3 of fluorescent substances corresponding to R, G and B color emissions were formed on the separate collector electrodes by the normal scheme.

Electron emission devices were assembled in such a manner that each pair of the device substrate and the transparent substrate are supported apart from one another by 10 mm in parallel by a spacer so that the thin-film metal electrode faces the collector electrode, with the clearance therebetween made to vacuum of  $10^{-7}$  Torr or  $10^{-5}$  Pa.

Then, the diode current  $I_d$  and the emission current  $I$  of each of the acquired plural devices with 400 nm thick insulator layer were measured with respect to the dielectric constant of materials while a driving voltage  $V_d$  of 0 to 200 V was applied to the prepared electron emission devices. Fig. 21 shows a relationship between a dielectric constant of the insulator layer and the emission current in the electron emission device. Fig. 22 shows a relationship between

a dielectric constant of insulator layer and the electron emission efficiency in the electron emission device. In these Figures, the plots of ■ denote the emission current  $I_e$  and the electron emission efficiency ( $I_e/I_d$ ) of the device having the insulator layer of fusion silica, □ denote amorphous  $\text{SiO}_x$ , ▲ denote Pyrex, △ denote  $\text{SiO}$ , ◆ denote  $\text{Ta}_2\text{O}_3$ , ◇ denote  $\text{Al}_2\text{O}_3$ , and ⊙ denote SiC respectively. As apparent from the results, there is a tendency that the emission current  $I_e$  and the electron emission efficiency ( $I_e/I_d$ ) decrease as the dielectric constant increases. It is understood that devices using the material having a dielectric constant of 8 or less, e.g., fusion silica,  $\text{Ta}_2\text{O}_3$ , amorphous  $\text{SiO}_x$ , Pyrex,  $\text{SiO}$  have an electron emission efficiency of  $1 \times 10^{-4}$  or more.

The maximum emission current  $I_e$  and the maximum electron emission efficiency ( $I_e/I_d$ ) were further measured with respect to the film thickness of the amorphous  $\text{SiO}_x$  layer while a driving voltage  $V_d$  of 0 to 200 V was applied to the prepared electron emission devices. Fig. 23 shows a relationship between a film thickness of insulator layer and the emission current  $I_e$  in the electron emission device. Fig. 24 shows a relationship between a film thickness of insulator layer and the electron emission efficiency ( $I_e/I_d$ ) in the electron emission device. As apparent from these Figures, the device whose amorphous  $\text{SiO}_x$  layer had a thickness of 50 to 1000 nm showed the maximum emission current of about  $1 \times 10^{-6}$  A/cm<sup>2</sup> or more and the maximum electron emission efficiency of about  $1 \times 10^{-1}$  under the application of 200V.

To obtain a good electron emission efficiency, it is understood that the insulator layer is made of a dielectric substance with a dielectric constant of 8 or less and has a film thickness of 50 nm or greater preferably 100 nm or greater in the electron emission device.

With a voltage of approximately 4 kV applied between the fluorescent-substance coated collector electrode and the thin-film metal electrode, a uniform fluorescent pattern corresponding to the shape of the thin-film metal electrode was observed in the device whose amorphous  $\text{SiO}_x$  layers have a thickness of 50 nm or greater. This shows that the electron emission from the amorphous  $\text{SiO}_x$  layer is uniform and has a high linearity. Therefore, those devices can serve as an electron emission diode or a light-emitting diode i.e., laser diode which emits electromagnetic waves of milli-wave or submilli-wave and furthermore as a high speed switching device.

As mentioned above, the use of a relatively low dielectric constant for the insulator layer is effective in the electron emission to achieve a high electron emission efficiency. Such dielectric materials includes  $\text{SiO}_2$ ,  $\text{SiO}_x$  ( $X = 1$  to 2), an alloy of  $\text{SiO}_2$  and  $\text{MgO}$ ,  $\text{ZnS}$ ,  $\text{CaF}_2$ ,  $\text{SrF}_2$ ,  $\text{SiN}$  and the like.

The change with respect to temperature of dielectric constant  $\epsilon_s$  of fusion silica (from Corning incorporated 7940) used in the above embodiment is measured after the thermal heating process. Table 2 shows as follows:

Table 2

Temperature of thermal process	Dielectric constant
30°C	3.7
300°C	3.8
500°C	3.85
800°C	3.9
1000°C	3.95
1200°C	3.98

As seen from Table 2, the change of the dielectric constant of fusion silica  $\text{SiO}_x$  is very small in a range 30°C to 1200°C and has a thermal stability.

The oxides of  $\text{Ta}_2\text{O}_3$ ,  $\text{ZrO}_2$ ,  $\text{TiO}_2$  were easily destructive and difficult in occurrence of negative resistance, and had a high conductivity in comparison with oxides of Si, AlN, so that the use of such former oxides is difficult for the electron emission device.

#### <Light emission at the side of device substrate>

Each of the foregoing examples of the electron emission devices on the first device substrate 10 is an opaque lamination comprising the electron supply layer 12, the insulator layer 13 and the thin-film metal electrode 15. The second transparent substrate 1 carries the transparent collector electrode 2 and the fluorescent layer(s) 3. The observer looks at emission light through the second transparent substrate by ordinary. Such a construction of the electron emission device provides only light passing through the transparent collector electrode 2 and the fluorescent layer(s) 3.

Thus, this example provides an electron emission device capable of emitting light through the first device substrate 10 of transparent glass on which, as shown in Fig. 25, the electron supply layer 12 is deposited at a thickness of 100 nm or less so as to be transparent due to a very thin-film formation. Furthermore, a light-permeable ohmic electrode



11 is also formed of indium tin oxide (so-called ITO), ZnO,  $\text{In}_2\text{O}_3$ , SrO or the like between the electron supply layer 12 and the first device substrate 10. In addition to the insulator layer 13 having a thickness of 50 nm preferably 100 to 400 nm thick, the lamination of the ohmic electrode 11, the insulator layer 13 and the thin-film metal electrode 15 is very thin as a whole. Therefore, the emitted light from the fluorescent layer(s) 3 fully passes through such a lamination on the first device substrate 10 of glass.

#### <Electron emission device with an alloy intermediate layer>

As shown in Fig. 26, an electron emission device of this embodiment has an electron supply layer 12 of metal or semiconductor such as Si or the like, an insulator layer 13 of  $\text{SiO}_x$  such as  $\text{SiO}_2$  or the like, an intermediate layer 14 of an Al-Li alloy and a thin-film metal electrode 15 of metal such as Au facing a vacuum space which are layered or formed in turn on an ohmic electrode 11 of Al previously formed on a device substrate 10. A pair of the device substrate 10 of a first substrate and a transparent substrate 1 are supported so as to face each other sandwiching a vacuum space therebetween. A transparent collector electrode 2 and a fluorescent layer 3R, 3G or 3B of fluorescent substance are formed in turn on the internal surface of the transparent substrate 1.

The material used for the thin-film metal electrode 15 is selected from substances each having a high electric resistance such as Au, Ag, Al, Cu which have a high work function of 4 (eV) or more. The material used for the intermediate layer 14 is selected from substances each having a work function equal to or lower than that of the thin-film metal electrode 15.

For example, a conductive material such as a metal of Ag, Al, Au, Ba, C, Ca, Cd, Cu, Fe, K, Li, Mg, Mo, Na, Nb, Ni, Pb, Pt, Sb, Si, Sn, Sr, Ta, Ti, Th, W, Zr, or Zn, or an alloy of Al-Li, In-Li, Mg-Sr, Al-Sr or Th-W, or an oxide of BaO, SrO or  $\text{ThO}_2$ , or a complex oxide such as  $\text{BaSrO}_2$ , or a boride such as  $\text{LaB}_6$ ,  $\text{CaB}_6$  or  $\text{SrB}_6$ , or a carbide such as ZrC, or ITO ( $\text{InO}_2\text{-SnO}_2$ ) can be used as the intermediate layer 14 disposed between the insulator layer 13 and the thin-film metal electrode 15. It should be noted that the material used for the intermediate layer 14 is selected from substances each having a work function equal to or lower than that of the thin-film metal electrode 15.

Electron emission devices according to the invention were fabricated and their characteristics were examined concretely.

An electron supply layer 12 of silicon (Si) was formed at 5000 nm thick, by sputtering, on an electrode surface of a device substrate 10 of glass on which an ohmic electrode of Al was previously formed 300 nm thick by sputtering. A plurality of Si substrates of this type were prepared.

Then, insulator layers 13 of  $\text{SiO}_2$  were formed on the electron supply layers 12 of the Si substrate by sputtering respectively while changing the film thickness of the insulator layer in a range from 0 nm to 500 nm. Thus a plurality of  $\text{SiO}_2$ -insulator substrates were provided. The  $\text{SiO}_2$  insulator layer was formed by sputtering by using a gas of Ar, Kr or Xe or a mixture thereof, or a gas mixture essentially consisting of one of those rare gases with  $\text{O}_2$ ,  $\text{N}_2$  or the like mixed therein, under the sputtering conditions of a gas pressure of 0.1 to 100 mTorr, preferably 0.1 to 20 mTorr and the forming rate of 0.1 to 1000 nm/min, preferably 0.5 to 100 nm/min. To obtain the single layer or multilayer structure, the amorphous or crystal phase, the grain size and the atomic ratio of the goal insulator layer 13, the sputtering device was controlled by properly altering the sputtering target and sputtering conditions.

The analysis on each  $\text{SiO}_2$  insulator layer in this embodiment by X-ray diffraction was performed and then the result showed a minor diffraction intensity Ic caused by the crystal portion and a major halo intensity Ia caused by the amorphous phase. It can be assumed from this result that  $\text{SiO}_2$  of the insulator layer mainly comprises of an amorphous phase portion.

Finally, an intermediate layer of an Al-Li alloy was formed at a thickness range of 10 to 100 nm on the surface of each amorphous  $\text{SiO}_2$  layer and then a thin-film metal electrode of Pt was formed at a 10 nm thick on the surface of the intermediate layer of each substrate by sputtering, thus providing plural device substrates with various thickness intermediate layers.

Meanwhile, transparent substrates 1 were prepared, each of which has ITO collector electrodes 2 formed inside thereof. Fluorescent layers 3 of fluorescent substances corresponding to Red, Green and Blue color emissions were formed on the collector electrodes 2 by the normal scheme respectively.

Electron emission devices were assembled by holding those device substrates and transparent substrates apart from one another by 10 mm in parallel by a spacer in such a way that the thin-film metal electrode faced the collector electrode, with the clearance therebetween made to vacuum of  $10^{-7}$  Torr or  $10^{-5}$  Pa.

Then, there were measured the diode current Id and the emission current Ie corresponding to the thickness of the  $\text{SiO}_2$  film of each of the acquired plural devices.

As a result, by applying a voltage of 200 V or lower, the emission current of  $1 \times 10^{-6}$  A/cm<sup>2</sup> or greater and the electron emission efficiency of  $1 \times 10^{-3}$  or greater were obtained from the electron emission device which has an  $\text{SiO}_2$  dielectric layer with 50 nm in thickness. Furthermore, the emission current of  $1 \times 10^{-3}$  A/cm<sup>2</sup> or greater and the electron emission efficiency of  $1 \times 10^{-1}$  or greater were obtained from the electron emission device which has an  $\text{SiO}_2$  dielectric

Ag

C  
ZrC

layer with 300 nm in thickness. Therefore, it was understood that the good emission current and electron emission efficiency were obtained in the devices each having an SiO<sub>2</sub> dielectric layer with 50 nm or greater, preferably 100 nm or greater in thickness.

With a voltage of approximately 4 kV applied between the fluorescent-substance coated collector electrode 2 and the thin-film metal electrode 15, a uniform fluorescent pattern corresponding to the shape of the thin-film metal electrode was observed in the devices whose SiO<sub>2</sub> layers have thicknesses of 50 nm or greater. This shows that the electron emission from the amorphous SiO<sub>2</sub> layer is uniform and has a high linearity, and that those devices can serve as an electron emission diode, or a light-emitting diode or laser diode which emits electromagnetic waves of infrared rays, visible light or ultraviolet rays.

According to the electron emission device of this embodiment according to the invention with the intermediate layer of a low work function, the insulator layer has a large thickness so that any holes are not likely to be produced, whereby the production yield is improved. The emission current of the electron emission device is greater than  $1 \times 10^{-6}$  A/cm<sup>2</sup> and is approximately  $1 \times 10^{-3}$  A/cm<sup>2</sup>, and the electron emission efficiency obtained is  $1 \times 10^{-1}$ . Therefore, this electron emission device, when in use in a display device, can provide a high luminance, can suppress the consumption of the drive current and the generation of heat from the device, and can reduce a burden on the driving circuit.

#### <Electron emission device including a region containing atoms or compounds of a low work function>

Inventors directed their attention to the relevant points to a region containing atoms or compounds of a low work function used for the intermediate layer in the above embodiments. Inventors fabricated various electron emission devices while altering the conditions of the region containing atoms or compounds of a low work function to improve the performance and stability of the electron emission devices and then examined concretely their characteristics thereof. As a result, Inventors revealed that there are effective matters that one of the thin-film metal electrode and the insulator layer is provided with a region containing atomic elements each having a work function such as the group I i.e., alkaline metals, the group II i.e., alkaline-earth metals, lanthanoids i.e., rare-earth elements or compounds combined thereto in order to improve the performance and stability of the electron emission device comprising an electron supply layer, an insulator layer and a thin-film metal electrode. This feature includes the above mentioned embodiment in which the intermediate layer of a low work function is disposed between the thin-film metal electrode and the insulator layer.

The atomic elements belonging to the group I, the group II and lanthanoids are preferably used for the region above mentioned because of their low work function in comparison with the other atomic elements. For example, their atomic symbols and their work functions (WORK FUNCTION) will be listed as follows: in the group I, Li(2.39), Na(2.27), K(2.15), Rb(2.13), Cs(1.89); in the group II, Be(3.37), Mg(3.46), Ca(2.76), Sr(2.35), Ba(2.29); in the lanthanoids, La(3.5), Ce(2.7), Pr(2.7), Nd(3.1), Pm, Sm(2.7), Eu(2.5), Gd(2.7), Tb(2.6), Dy(2.2), Ho(2.3), Er(2.4), Tm, Yb, Lu(3.3).

The single elements above listed or compounds combined thereto may be used as a low work function substances. Such elements and compounds are as follows: with respect to the group I a, LiO<sub>2</sub>, LiI, Li<sub>2</sub>SiO<sub>3</sub>, Li<sub>2</sub>TiO<sub>3</sub>, Na<sub>2</sub>O, NaI, NaFeO<sub>2</sub>, Na<sub>4</sub>SiO<sub>4</sub>, K<sub>2</sub>O, KI, K<sub>2</sub>TiO<sub>3</sub>, K<sub>2</sub>WO<sub>4</sub>, Rb<sub>2</sub>O, RbBr, Rb<sub>2</sub>CrO<sub>4</sub>, Cs<sub>2</sub>O, CsBr, CsCrO<sub>4</sub>; with respect to the group IIa, BeO, BeY<sub>2</sub>O<sub>4</sub>, BeNb<sub>2</sub>O<sub>4</sub>, BeIn<sub>2</sub>O<sub>4</sub>, BeTa<sub>2</sub>O<sub>6</sub>, MgO, Mg<sub>2</sub>Ag, MgY<sub>2</sub>O<sub>4</sub>, MgNb<sub>2</sub>O<sub>4</sub>, MgIn<sub>2</sub>O<sub>4</sub>, MgTa<sub>2</sub>O<sub>6</sub>, CaO, Ca<sub>2</sub>Cu, CaY<sub>2</sub>O<sub>4</sub>, CaNb<sub>2</sub>O<sub>4</sub>, CaIn<sub>2</sub>O<sub>6</sub>, CaTa<sub>2</sub>O<sub>6</sub>, SrO, SrS, SrY<sub>2</sub>O<sub>4</sub>, SrNb<sub>2</sub>O<sub>4</sub>, SrIn<sub>2</sub>O<sub>4</sub>, SrTa<sub>2</sub>O<sub>6</sub>, BaO, BaTiO<sub>3</sub>, BaY<sub>2</sub>O<sub>4</sub>, BaNb<sub>2</sub>O<sub>4</sub>, BaIn<sub>2</sub>O<sub>6</sub>, BaTa<sub>2</sub>O<sub>6</sub>; and with respect to the lanthanoids, La<sub>2</sub>O<sub>3</sub>, BeLa<sub>2</sub>O<sub>4</sub>, MgLa<sub>2</sub>O<sub>4</sub>, CaLa<sub>2</sub>O<sub>4</sub>, SrLa<sub>2</sub>O<sub>4</sub>, BaLa<sub>2</sub>O<sub>4</sub>, Ce<sub>2</sub>O<sub>3</sub>, BeCe<sub>2</sub>O<sub>4</sub>, MgCe<sub>2</sub>O<sub>4</sub>, CaCe<sub>2</sub>O<sub>4</sub>, SrCe<sub>2</sub>O<sub>4</sub>, BaCe<sub>2</sub>O<sub>4</sub>, Pr<sub>2</sub>O<sub>3</sub>, BePr<sub>2</sub>O<sub>4</sub>, MgPr<sub>2</sub>O<sub>4</sub>, CaPr<sub>2</sub>O<sub>4</sub>, SrPr<sub>2</sub>O<sub>4</sub>, BaPr<sub>2</sub>O<sub>4</sub>, Nd<sub>2</sub>O<sub>3</sub>, BeNd<sub>2</sub>O<sub>4</sub>, MgNd<sub>2</sub>O<sub>4</sub>, CaNd<sub>2</sub>O<sub>4</sub>, SrNd<sub>2</sub>O<sub>4</sub>, BaNd<sub>2</sub>O<sub>4</sub>, Sm<sub>2</sub>O<sub>3</sub>, BeSm<sub>2</sub>O<sub>4</sub>, MgSm<sub>2</sub>O<sub>4</sub>, CaSm<sub>2</sub>O<sub>4</sub>, SrSm<sub>2</sub>O<sub>4</sub>, BaSm<sub>2</sub>O<sub>4</sub>, Eu<sub>2</sub>O<sub>3</sub>, BeEu<sub>2</sub>O<sub>4</sub>, MgEu<sub>2</sub>O<sub>4</sub>, CaEu<sub>2</sub>O<sub>4</sub>, SrEu<sub>2</sub>O<sub>4</sub>, BaEu<sub>2</sub>O<sub>4</sub>, Gd<sub>2</sub>O<sub>3</sub>, BeGd<sub>2</sub>O<sub>4</sub>, MgGd<sub>2</sub>O<sub>4</sub>, CaGd<sub>2</sub>O<sub>4</sub>, SrGd<sub>2</sub>O<sub>4</sub>, BaGd<sub>2</sub>O<sub>4</sub>, Tb<sub>2</sub>O<sub>3</sub>, BeTb<sub>2</sub>O<sub>4</sub>, MgTb<sub>2</sub>O<sub>4</sub>, CaTb<sub>2</sub>O<sub>4</sub>, SrTb<sub>2</sub>O<sub>4</sub>, BaTb<sub>2</sub>O<sub>4</sub>, Dy<sub>2</sub>O<sub>3</sub>, BeDy<sub>2</sub>O<sub>4</sub>, MgDy<sub>2</sub>O<sub>4</sub>, CaDy<sub>2</sub>O<sub>4</sub>, SrDy<sub>2</sub>O<sub>4</sub>, BaDy<sub>2</sub>O<sub>4</sub>, Ho<sub>2</sub>O<sub>3</sub>, BeHo<sub>2</sub>O<sub>4</sub>, MgHo<sub>2</sub>O<sub>4</sub>, CaHo<sub>2</sub>O<sub>4</sub>, SrHo<sub>2</sub>O<sub>4</sub>, BaHo<sub>2</sub>O<sub>4</sub>, Er<sub>2</sub>O<sub>3</sub>, BeEr<sub>2</sub>O<sub>4</sub>, MgEr<sub>2</sub>O<sub>4</sub>, CaEr<sub>2</sub>O<sub>4</sub>, SrEr<sub>2</sub>O<sub>4</sub>, BaEr<sub>2</sub>O<sub>4</sub>, Tm<sub>2</sub>O<sub>3</sub>, BeTm<sub>2</sub>O<sub>4</sub>, MgTm<sub>2</sub>O<sub>4</sub>, CaTm<sub>2</sub>O<sub>4</sub>, SrTm<sub>2</sub>O<sub>4</sub>, BaTm<sub>2</sub>O<sub>4</sub>, Yb<sub>2</sub>O<sub>3</sub>, BeYb<sub>2</sub>O<sub>4</sub>, MgYb<sub>2</sub>O<sub>4</sub>, CaYb<sub>2</sub>O<sub>4</sub>, SrYb<sub>2</sub>O<sub>4</sub>, BaYb<sub>2</sub>O<sub>4</sub>, Lu<sub>2</sub>O<sub>3</sub>, BeLu<sub>2</sub>O<sub>4</sub>, MgLu<sub>2</sub>O<sub>4</sub>, CaLu<sub>2</sub>O<sub>4</sub>, SrLu<sub>2</sub>O<sub>4</sub>, BaLu<sub>2</sub>O<sub>4</sub>. The compounds used as a low work function substances in the invention include oxides, nitrides, complex oxides, alloys, salts and inorganic compounds. The work function of the compound is dependent on the included atom having a lowest work function. Furthermore, the substance mentioned above in the embodiments is included in the low work function substances.

#### <Another embodiment including a region containing atoms or compounds of a low work function>

A region containing the other atoms or compounds of a low work function than the above may be formed between the insulator layer 13 and the thin-film metal electrode 15. In this case, the intermediate layer is an alloy of the group I, the group II, or the mixture thereof. The intermediate layer may be enhanced as a compound to be in an island-shape capable of lamination to facilitate the transferring of electrons from the insulator to the thin-film metal electrode.

*Motivation*

The intermediate layer was formed by sputtering by using a gas of Ar, Kr or Xe or a mixture thereof under the sputtering conditions of a gas pressure of 1 to 20 mTorr, preferably 0.4 to 40 mTorr and the forming rate of 0.1 to 10 nm/sec. for a pertinent time period. After the film-forming, there are a shape of many islands dispersed instead of a layer-shape but such an island-shape is effective for the intermediate layer.

Concretely, an electron emission device was manufactured through the same manner as the above embodiments with the intermediate layer except that the low work function substance of  $Tb_4O_7$  is used for each intermediate layer while controlling the sputter conditions of the gas pressure of 5 mTorr or the forming rate i.e., deposition rate 0.5 nm/sec for a forming time period of 2 seconds. Then, there were measured the diode current  $I_d$  and the emission current  $I_e$  corresponding to the film thickness of the  $SiO_2$  insulator layer.

In the case that a driving voltage  $V_d$  of 0 to 100 V was applied to the prepared electron emission devices, Fig. 27 shows a relationship between a film thickness of insulator layer and the maximum emission current  $I_e$  in the electron emission device, and Fig. 28 shows a relationship between a film thickness of insulator layer and the maximum electron emission efficiency ( $I_e/I_d$ ) in the electron emission device. As apparent from these Figures, the device having the 50 nm or greater thick insulator layer shows an effective emission current  $I_e$ . The device whose insulator layer having a thickness of 400 to 700 nm showed the maximum emission current of about  $1 \times 10^{-3}$  A/cm<sup>2</sup> and the maximum electron emission efficiency of about  $1 \times 10^{-1}$ .

Fig. 29 shows a property relationship between the emission current  $I_e$  and the driving voltage  $V_d$  of the electron emission devices both of the embodiment and the comparative electron emission when a driving voltage  $V_d$  of 0 to 100 V was applied to each prepared electron emission device. In Figure, the plots A denote a property of emission current of the device having the intermediate layer 14 and the plots B denote that of the comparative device assembled in the same manner except having non-intermediate layer. As seen from Fig. 29, the device having the intermediate layer began to emit electrons at a driving voltage  $V_d$  of about 55 V. In contrast, the device without any intermediate layer began to emit electrons at a driving voltage  $V_d$  of about 70 V or more. In this way, it is understood that the driving voltage of the electron emission device having the intermediate layer is reduced in comparison with the comparative.

<Electron emission device including an electron emission layer as a region containing atoms or compounds of a low work function>

Fig. 30 is an partly enlarged schematic cross-sectional view showing an electron emission device of another embodiment according to the invention which has an electron emission layer 14a disposed on an outer surface of the thin-film metal electrode 15. The electron emission device is manufactured through the same manner as the above embodiments except that the electron emission layer 14a is formed on the outer surface (facing a vacuum space) of the thin-film metal electrode 15, instead of the intermediate layer 14 of Al-Li alloy or  $Tb_4O_7$  disposed between the insulator layer 13 and the thin-film metal electrode 15 in the above embodiment shown in Fig. 26. The electron emission layer 14a was formed at a thickness of 1 nm or less. In this case, the electron emission layer 14a was formed so as to be many island-shaped regions dispersed on the surface of the thin-film metal electrode 15 rather than a layer. The thin-film metal electrode 15 can lower the potential barrier.

In the above electron emission device having the lamination structure of the electron emission layer 14a of a low work function, the thin-film metal electrode 15 and the insulator layer 13 are formed in turn. The material of the electron emission layer 14a may be preferably selected from the group I, the group II, lanthanides or a compound thereof. The thickness of the electron emission layer 14a may be set within 10 nm or less, otherwise the emission current decreases due to lost energy of electrons. This is because the electron emission layer is likely to scatter electrons passing there-through when it has a thickness more than 10 nm.

The thin-film formation of the electron emission layer 14a may be performed through a sputtering method by using a sputter gas of Ar, Kr, Xe or the like under the sputtering conditions of a gas pressure of 1 to 20 mTorr and a forming rate of 0.1 to 10 nm/sec preferably while the thickness of the electron emission layer 14a is controlled by the thin-film formation time period. The analysis of the resultant electron emission layer 14a showed a film-growth in a multi-island shape as shown in Fig. 30 instead of a continuous layer. Nevertheless the multi-island shaped electron emission layer 14a has a sufficient effect to emit electrons. There are the other thin-film formation such as vacuum deposition, CVD, laser ablation, MBE and ion beam sputtering which are effective.

Concretely, an electron emission device with a multi-island shaped electron emission layer 14a as shown in Fig. 30 was manufactured through the same manner as the above embodiments except that the electron emission layer of  $Tb_4O_7$  was formed on the thin-film metal electrode 15. In this case, while controlling the sputter conditions of the gas pressure of 5 mTorr or the deposition rate 0.5 nm/sec,  $Tb_4O_7$  was disposed for a forming time period of 2 seconds.

Then, there was measured the diode current  $I_d$  and the emission current  $I_e$  corresponding to the film thickness of the  $SiO_2$  insulator layer.

In the case that a driving voltage  $V_d$  of 0 to 100 V was applied to the prepared electron emission devices, Figs. 31 and 32 shows relationships between a film thickness of insulator layer and both the maximum emission current  $I_e$  and

the maximum electron emission efficiency ( $I_e/I_d$ ) in the electron emission device. As apparent from these Figures, the device whose insulator layer having a thickness of 400 to 700 nm showed the maximum emission current of about  $1 \times 10^{-3}$  A/cm<sup>2</sup> and the maximum electron emission efficiency of about  $1 \times 10^{-1}$ . To obtain a good electron emission efficiency, it is understood that the insulator layer has a film thickness of 50 nm or greater.

Fig. 33 shows a property relationship between the emission current  $I_e$  and the driving voltage  $V_d$  of the electron emission devices both of the embodiment and the comparative electron emission when a driving voltage  $V_d$  of 0 to 100 V was applied to each prepared electron emission device. In Figure, the plots A denote a property of emission current of the device having the electron emission layer and the plots B denote that of the comparative device assembled in the same manner except that no electron emission layer is provided. As seen from Fig. 33, the device having the electron emission layer began to emit electrons at a driving voltage  $V_d$  of about 50 V. In contrast, the device without any electron emission layer began to emit electrons at a driving voltage  $V_d$  of about 70 V or more. In this way, it is understood that the driving voltage of the electron emission device having the electron emission layer is reduced in comparison with the comparative.

<Electron emission device including a region containing atoms or compounds of a low work function which is formed within the thin-film metal electrode>

Fig. 34 is a partly enlarged schematic cross-sectional view showing an electron emission device of another embodiment according to the invention in which the thin-film metal electrode has a region 14b containing atomic elements each having a work function which is lower than that of the thin-film metal electrode 15. The electron emission device is manufactured through the same manner as the above embodiments except that the low work function regions 14b are disposed within the thin-film metal electrode 15, instead of the intermediate layer 14 of Al-Li alloy or Tb<sub>4</sub>O<sub>7</sub> disposed between the insulator layer 13 and the thin-film metal electrode 15 in the above embodiment shown in Fig. 26. In this case, the low work function regions 14b can lower the potential barrier and facilitate to transfer electrons from the insulator layer to the thin-film metal electrode.

In the electron emission device in which the low work function substances 14b are disposed in the thin-film metal electrode 15 formed on the insulator layer 13, the matrix of the thin-film metal electrode 15 serves as a thin-film applying a uniform electric field to a constant area of the surface of the device. It is therefore necessary for the matrix to employ a material having an electric resistance of  $10^{-3}$  Ωcm or less. For example the matrix of the thin-film metal electrode 15 is selected from a substance having a  $10^{-6}$  Ωcm order such as Al, Au, Pt, Cu, Pd, Ag, Ni, W, Ir, Fe, Co, and Ti. The low work function substances 14b are added to such a selected matrix to obtain the thin-film metal electrode 15. The mixture ratio of the low work function substances 14b added to the thin-film metal electrode 15 is dependent on the electric resistance of the matrix. The addition of the low work function substance to the matrix of the is controlled so that the thin-film metal electrode 15 has an electric resistance of  $10^{-3}$  Ωcm or less.

The thin-film formation of the thin-film metal electrode 15 containing the low work function substances 14b may be performed through a sputtering method. In this case, a target of a pertinent mixture above mentioned may be used. A complex target of a major component target including an additive of a low work function substance may be also used. Alternatively, a co-sputtering method using two source targets may be employed in which a major component target and an additive target of a low work function substance are used.

Concretely, an electron emission device having the thin-film metal electrode 15 containing the low work function substances 14b as shown in Fig. 34 was manufactured through the same manner as the above embodiments except that the thin-film metal electrode was formed of a major component Au and a minor component Tb<sub>4</sub>O<sub>7</sub> of a low work function on the insulator layer. In this case, the co-sputtering was used and the resultant thin-film metal electrode had  $2 \times 10^{-5}$  Ωcm.

Then, there was measured the diode current  $I_d$  and the emission current  $I_e$  corresponding to the film thickness of the SiO<sub>2</sub> insulator layer.

In the case that a driving voltage  $V_d$  of 0 to 100 V was applied to the prepared electron emission devices, Figs. 35 and 36 show relationships between a film thickness of insulator layer and both the maximum emission current  $I_e$  and the maximum electron emission efficiency ( $I_e/I_d$ ) in the electron emission device respectively. As apparent from these Figures, the device whose insulator layer having a thickness of 400 to 700 nm showed the maximum emission current of about  $1 \times 10^{-3}$  A/cm<sup>2</sup> and the maximum electron emission efficiency of about  $1 \times 10^{-1}$ . To obtain a good electron emission efficiency, it is understood that the insulator layer has a film thickness of 50 nm or greater.

Fig. 37 shows a property relationship between the emission current  $I_e$  and the driving voltage  $V_d$  of the electron emission devices both of the embodiment and the comparative electron emission when a driving voltage  $V_d$  of 0 to 100 V was applied to each prepared electron emission device. In Figure, the plot A denotes a property of emission current of the embodiment having the thin-film metal electrode containing the low work function substances and, the plots B denote that of the comparative device assembled in the same manner except that non-low work function substance is provided. As seen from Fig. 37, the device having the thin-film metal electrode containing the low work function

substances began to emit electrons at a driving voltage  $V_d$  of about 50 V. In contrast, the device without any low work function substance began to emit electrons at a driving voltage  $V_d$  of about 70 V or more. In this way, it is understood that the driving voltage of the electron emission device having the low work function substance in the thin-film metal electrode is reduced in comparison with the comparative.

Moreover, fluctuations of the emission current  $I_e$  of the electron emission device both of the embodiment and the comparative device are measured. Fig. 38 shows the fluctuation of emission current with the laps of time in the electron emission device having the low work function substance in the thin-film metal electrode. Fig. 39 shows the same of the comparative electron emission device without any low work function substance in the thin-film metal electrode. As apparent from these Figures, the fluctuations of the emission current  $I_e$  of the electron emission device of the embodiment is very small in comparison with that of the comparative device. It is assumed that this phenomenon occurs due to the thin-film metal electrode including the dispersed low work function substances is capable of facilitating to pass many electrons from the electron supply layer to the thin-film metal electrode in stable in comparison with the comparative device without any low work function substance.

Fig. 40 shows a property relationship of a diode current  $I_d$  and the emission current  $I_e$  with respect to the driving voltage  $V_d$  of the electron emission device. From this, it is understood that the diode current  $I_d$  shows a hysteresis property. In Figure, a voltage drop of diode current  $I_d$  occurs at the starting point of the emission current  $I_e$  and then the emission current  $I_e$  rises.

#### <Electron emission device including a region containing atoms or compounds of a low work function which is formed within the insulator layer>

Fig. 41 is an partly enlarged schematic cross-sectional view showing an electron emission device of another embodiment according to the invention in which the insulator layer 13 has a region 14c containing atomic elements each having a low work function. The electron emission device is manufactured through the same manner as the above embodiments except that the low work function regions 14c are disposed within the insulator layer 13, instead of the intermediate layer 14 of Al-Li alloy or  $Tb_4O_7$  disposed between the insulator layer 13 and the thin-film metal electrode 15 in the above embodiment shown in Fig. 26. In this case, the low work function regions 14c can facilitate to transfer electrons from the insulator layer 13 to the thin-film metal electrode 15. The low work function regions 14c may be formed in such a manner that the higher the concentration distribution of low work function substances becomes closer to the thin-film metal electrode 15. The thin-film formation of the insulator layer 13 with the low work function regions 14c is the same as that of the device shown in Fig. 34.

As mentioned above, in the electron emission device comprising an electron supply layer, an insulator layer and a thin-film metal electrode, the improvement of the performance and stableness is achieved by providing a region containing atomic elements each having a work function such as the group I i.e., alkaline metals, the group II i.e., alkaline-earth metals, lanthanoids i.e., rare-earth elements or compounds combined thereto within one of the thin-film metal electrode and the insulator layer or therebetween.

#### <Electron emission device including an insulator layer having a thermal conductive layers>

As shown in Fig. 42, an electron emission device of this embodiment has an electron supply layer 12 of metal or semiconductor, an insulator layer 13 with a thermal conductive layer 14, and a thin-film metal electrode 15 facing a vacuum space which are layered or formed in turn on an ohmic electrode 11 of Al previously formed on a device substrate 10. The insulator layer 13 is made of a dielectric substance and has a very large film thickness of 50 nm or greater. The electron emission device is a diode with the top electrode i.e., the thin-film metal electrode 15 set to a positive potential  $V_d$  and the back electrode i.e., the ohmic electrode 11 set to a ground potential. The electron emission device emits electrons when an electric field is applied between the electron supply layer and the thin-film metal electrode. When the driving voltage  $V_d$  is applied between the ohmic electrode 11 and the thin-film metal electrode 15 to supply electrons into the electron supply layer 12, a diode current  $I_d$  flows. Since the insulator layer 13 has a high resistance, most of the applied electric field is applied to the insulator layer 13. The electrons move inside the insulator layer 13 toward the thin-film metal electrode 15. Some of the electrons that reach near the thin-film metal electrode 15 tunnel through the thin-film metal electrode 15, due to the strong electric field, to be discharged out into the vacuum space. The electrons  $e$  (emission current  $I_e$ ) discharged from the thin-film metal electrode 15 by the tunnel effect are accelerated by a high voltage  $V_c$ , which is applied to the opposing transparent collector electrode 2, and is collected at the collector electrode 2. When the fluorescent substance is coated on the collector electrode 2, corresponding visible light is emitted.

For the material of the thermal conductive layer 14 provided within the insulator layer, a substance i.e., metal, metal oxide, or alloy having a good thermal conductivity higher than that of the insulator layer such as Ag, Al,  $Al_2O_3$ , Au, Be, BeO, C, Ca, Cd, CdS, Cr, Cu, Fe, In, InSb, Ir, K, Li, Mg, Mo,  $NH_4Cl$ , Na, Nb, Ni, NiO, Pb, Pt, Rh, Sb, Si, Sn,

Ta, Ti, TiO<sub>2</sub>, Ti, TiCl, W, Zn, Zr, brass, Manganin™, Constantan, stainless steel, nichrome, Inconel, Monel, Pt-10%Rhodium can be used.

Electron emission devices of this embodiment were fabricated and their characteristics were examined concretely.

An electron supply layer of silicon (Si) was formed at 5000 nm thick, by sputtering, on an electrode surface of a device substrate 10 of glass on which an ohmic electrode of Al was previously formed 300 nm thick by sputtering. A plurality of Si substrates of this type were prepared.

Then, insulator layers of SiO<sub>2</sub> were formed on the electron supply layers of the Si substrate by sputtering respectively while changing the film thickness of the insulator layer in a range from 0 nm to 500 nm. Thus a plurality of SiO<sub>2</sub>-insulator substrates were provided. Each SiO<sub>2</sub> insulator layer was formed by sputtering by using a gas of Ar, Kr or Xe or a mixture thereof, or a gas mixture essentially consisting of one of those rare gases with O<sub>2</sub>, N<sub>2</sub>, H<sub>2</sub> or the like mixed therein, under the sputtering conditions of a gas pressure of 0.1 to 100 mTorr, preferably 0.1 to 20 mTorr and the forming rate of 0.1 to 1000 nm/min, preferably 0.5 to 100 nm/min. To obtain the single layer or multilayer structure, the amorphous or crystal phase, the grain size and the atomic ratio of the insulator layer 13, the sputtering device was controlled by properly altering the sputtering target and sputtering conditions. A thermal conductive layer of Al was formed at a 10 nm thick by sputtering at the intermediate point during sputtering of the insulator layer so as to be sandwiched therebetween.

The thickness of the thermal conductive layer may be set within a range less than that of the insulator layer and 1 nm or more.

Finally, a thin-film metal electrode of Pt was formed at a 10 nm thick on the surface of the amorphous SiO<sub>2</sub> layer of each substrate by sputtering, thus providing plural device substrates.

Meanwhile, transparent substrates were prepared, each of which has ITO collector electrodes formed inside the transparent glass substrate. Fluorescent layers of fluorescent substances corresponding to Red, Green and Blue color emissions were formed on the collector electrodes by the normal scheme respectively.

Electron emission devices were assembled by holding those device substrates and transparent substrates apart from one another by 10 mm in parallel by a spacer in such a way that the thin-film metal electrode faced the collector electrode, with the clearance therebetween made to a vacuum of 10<sup>-7</sup> Torr or 10<sup>-5</sup> Pa.

Then, the diode current Id and the emission current Ie corresponding to the thickness of the SiO<sub>2</sub> film of each of the acquired plural devices were measured.

Figs. 43 and 44 show the relationships both between the film thickness of each SiO<sub>2</sub> layer and the maximum emission current Ie and between that film thickness and the maximum electron emission efficiency (Ie/Id) for each film thickness respectively when a driving voltage Vd of 0 to 200 V was applied to the prepared electron emission devices. As apparent from these Figures, while the emission current and the electron emission efficiency were saturated from the thickness of 50 nm, the devices whose SiO<sub>2</sub> layers had thicknesses of 300 to 500 nm showed the maximum emission current of about 1 x 10<sup>-5</sup> A/cm<sup>2</sup> and the maximum electron emission efficiency of about 1 x 10<sup>-1</sup>.

The temperature of resultant electron emission device was measured for 10 seconds or more. After some comparative electron emission devices were fabricated in the same manner of this embodiment except that there is no thermal conductive layer in the insulator layer, the temperature of resultant electron emission device was measured similarly. Fig. 45 shows the results that both of the electron emission device of this embodiment and the comparative change in temperature with the lapse of time. In Figure, the curve A denotes a temperature property of the device having the thermal conductive layer in the insulator layer, and the curve B denotes that of thermal conductive layer. As seen from this Figure, the device of the embodiment having the thermal conductive layer is stable at a low temperature in comparison with the device without any thermal conductive layer.

Moreover, fluctuations of the emission current Ie of the electron emission device both of the embodiment and the comparative are measured. Fig. 46A shows the fluctuation of emission current with the lapse of time in the electron emission device having a thermal conductive layer, and Fig. 46B shows the same of the comparative electron emission device having no thermal conductive layer. As apparent from these Figures, the thermal conductive layer disposed in the insulator layer of the electron emission device can restrict the fluctuation of Ie to make the emission current of the electron emission device stable in comparison with the comparative. Further, it is understood that the embodiment has a life longer than that of the comparative which is broken during this examination.

<Electron emission display device>

Fig. 47 shows an electron emission display device according to one embodiment of the invention. This embodiment comprises a pair of the transparent substrate 1 and the device substrate 10, which face each other with a vacuum space in between. In the illustrated electron emission display apparatus, a plurality of transparent collector electrodes 2 of, for example, an indium tin oxide (so-called ITO), tin oxide (SnO), zinc oxide (ZnO) or the like, are formed in parallel on the inner surface of the transparent glass substrate 1 or the display surface (which faces the back substrate 10). The collector electrodes 2 may be formed integrally. The transparent collector electrodes which trap emitted electrons

are arranged in groups of three in association with red (R), green (G) and blue (B) color signals in order to provide a color display panel, and voltages are applied to those three collector electrodes respectively. Therefore, fluorescent layers 3R, 3G and 3B of fluorescent substances corresponding to R, G and B color emissions are respectively formed on the three collector electrodes 2 in such a way as to face the vacuum space 4.

A plurality of ohmic electrodes 11 are formed in parallel on the inner surface of the device substrate 10 of glass or the like which faces the transparent glass substrate 1 with the vacuum space 4 in between (i.e., said inner surface faces the transparent glass substrate 1) via an auxiliary insulator layer 18. The auxiliary insulator layer 18 is comprised of an insulator such as  $\text{SiO}_2$ ,  $\text{SiN}_x$ ,  $\text{Al}_2\text{O}_3$  or  $\text{AlN}$ , and serves to prevent an adverse influence of the device substrate 10 on the device (such as elution of an impurity such as an alkaline component or a roughened substrate surface). A plurality of electron emission devices S are formed on the ohmic electrodes 11. In order to adjoining thin-film metal electrodes 15 are electrically connected to each other, a plurality of bus electrodes 16 are formed on parts of the thin-film metal electrodes 15, extending in parallel to one another and perpendicular to the ohmic electrodes 11. Each electron emission device S comprises the electron supply layer 12, the insulator layer 13 and the thin-film metal electrode 15 which are formed in order on the associated ohmic electrode 11.

The thin-film metal electrodes 15 face the vacuum space 4. A second auxiliary insulator layer 17 with openings is formed to separate the surfaces of the thin-film metal electrodes 15 into a plurality of electron emission regions. This second auxiliary insulator layer 17 covers the bus electrodes 16 to prevent unnecessary short-circuiting.

The material for the ohmic electrodes 11 is Au, Pt, Al, W or the like which is generally used for the wires of an IC, and has a uniform thickness for supplying substantially the same current to the individual devices.

While silicon (Si) is one material for the electron supply layer 12, it is not restrictive for the electron supply layer of the invention and other semiconductors or metals of any of amorphous, polycrystal and monocrystal can be used as well.

From the principle of electron emission, it is better that the material for the thin-film metal electrode 15 has a lower work function  $\phi$  and is thinner. To increase the electron emission efficiency, the material for the thin-film metal electrode 15 should be a metal of the group I or group II in the periodic table; for example, Cs, Rb, Li, Sr, Mg, Ba, Ca and the like are effective and alloys of those elements may be used as well. To make the thin-film metal electrode 15 very thin, the material for the thin-film metal electrode 15 should be a chemically stable metal with a high conductivity; for example, single substances of Au, Pt, Lu, Ag and Cu or alloys thereof are desirable. It is effective to coat or dope a metal with a low work function as described above on or in those metals.

The material for the bus electrodes 16 can be Au, Pt, Al or the like which is generally used for the wiring of an integrated circuit IC, and should have a thickness enough to supply substantially the same potential to the individual devices, adequately of 0.1 to 50  $\mu\text{m}$ .

A simple matrix system or an active matrix system may be employed as the driving system for the display device of the invention.

In the electron emission display device according to the invention as shown in Fig. 47, it should be understood that the intermediate layer (not shown) is formed between the insulator layer 13 and the thin-film metal electrodes 15, and that the electron emission layer (not shown) is formed on the thin-film metal electrodes 15, and that a region containing atoms or compounds of a low work function (not shown) is formed within the insulator layer 13 or the thin-film metal electrodes 15, and that the thermal conductive layer (not shown) is formed within the insulator layer 13. Furthermore, a plurality of thermal conductive layers may be formed within the insulator layer 13, although the above embodiment is described for only a single thermal conductive layer to simplify the examination of the device. In addition, other functional layer such as Si,  $\text{SiO}_x$  may be also formed in the form of plural layers laminated to one another.

## Claims

### 1. An electron emission device comprising:

an electron supply layer of metal or semiconductor;  
an insulator layer formed on the electron supply layer; and  
a thin-film metal electrode formed on the insulator layer and facing a vacuum space,  
characterized in that said insulator layer is made of a dielectric substance and has a film thickness of 50 nm or greater, whereby the electron emission device emits electrons when an electric field is applied between the electron supply layer and the thin-film metal electrode.

### 2. An electron emission display device comprising:

a pair of first and second substrates facing each other with a vacuum space in between;

a plurality of electron emission devices provided on the first substrate;  
 a collector electrode provided in the second substrate; and  
 a fluorescent layer formed on the collector electrode,  
 each of the electron emission devices comprising an electron supply layer of metal or semiconductor;  
 5 an insulator layer formed on the electron supply layer; and a thin-film metal electrode formed on the insulator  
 layer and facing a vacuum space, wherein said insulator layer is made of a dielectric substance and has a  
 film thickness of 50 nm or greater.

3. An electron emission display device comprising:

a pair of a device substrate and a transparent substrate facing each other with a vacuum space in between;  
 a plurality of ohmic electrodes formed in parallel on an inner surface of the device substrate;  
 a plurality of electron emission devices provided on the ohmic electrodes, each of the electron emission devices  
 15 comprising an electron supply layer of metal or semiconductor; an insulator layer formed on the electron supply  
 layer; and a thin-film metal electrode formed on the insulator layer and facing the vacuum space, wherein said  
 insulator layer is made of a dielectric substance and has a film thickness of 50 nm or greater;  
 a plurality of bus electrodes formed on parts of the thin-film metal electrodes and extending in parallel to one  
 another and perpendicular to the ohmic electrodes so as to electrically connect adjoining thin-film metal elec-  
 20 trodes;  
 a plurality of collector electrodes provided in the transparent substrate; and  
 fluorescent layers formed on the collector electrodes.

4. An electron emission display device according to claim 3 further comprising a second auxiliary insulator layer with  
 25 openings formed to separate the surfaces of the thin-film metal electrodes into a plurality of electron emission  
 regions.

5. An electron emission display device according to claim 4, wherein the second auxiliary insulator layer covers the  
 bus electrodes.

6. An electron emission device comprising:

an electron supply layer of metal or semiconductor;  
 an insulator layer formed on the electron supply layer; and  
 a thin-film metal electrode formed on the insulator layer and facing a vacuum space,  
 35 characterized in that said insulator layer is made of a dielectric substance with a dielectric constant of 8 or  
 less and has a film thickness of 50 nm or greater, whereby the electron emission device emits electrons when  
 an electric field is applied between the electron supply layer and the thin-film metal electrode.

7. An electron emission display device comprising:

a pair of first and second substrates facing each other with a vacuum space in between;  
 a plurality of electron emission devices provided on the first substrate;  
 a collector electrode provided in the second substrate; and  
 a fluorescent layer formed on the collector electrode,  
 45 each of the electron emission devices comprising an electron supply layer of metal or semiconductor; an  
 insulator layer formed on the electron supply layer; and a thin-film metal electrode formed on the insulator  
 layer and facing a vacuum space, wherein said insulator layer is made of a dielectric substance with a dielectric  
 constant of 8 or less and has a film thickness of 50 nm or greater.

8. An electron emission device comprising:

an electron supply layer of metal or semiconductor;  
 an insulator layer formed on the electron supply layer; and  
 a thin-film metal electrode formed on the insulator layer and facing a vacuum space,  
 55 characterized in that said insulator layer has a film thickness of 50 nm or greater and said electron supply  
 layer is transparent, whereby the electron emission device emits electrons when an electric field is applied  
 between the electron supply layer and the thin-film metal electrode.



9. An electron emission display device comprising:

a pair of first and second substrates facing each other with a vacuum space in between;  
 a plurality of electron emission devices provided on the first substrate;  
 5 a collector electrode provided in the second substrate; and  
 a fluorescent layer formed on the collector electrode,  
 each of the electron emission devices comprising an electron supply layer of metal or semiconductor; an  
 insulator layer formed on the electron supply layer; and a thin-film metal electrode formed on the insulator  
 layer and facing a vacuum space, wherein said insulator layer has a film thickness of 50 nm or greater and  
 10 said electron supply layer is transparent.

10. An electron emission display device according to claim 9, wherein said first substrate is transparent, and further  
 comprising a transparent electrode which is disposed between said first substrate and said electron supply layer.

15 11. An electron emission device comprising:

an electron supply layer of metal or semiconductor;  
 an insulator layer formed on the electron supply layer; and  
 a thin-film metal electrode formed on the insulator layer and facing a vacuum space,  
 20 characterized in that said insulator layer is made of silicon oxide as a major component of  $\text{SiO}_x$  where x represents an atomic ratio in a range of from 0.5 to 2 in a whole layer and has a film thickness of 50 nm or greater,  
 whereby the electron emission device emits electrons when an electric field is applied between the electron  
 supply layer and the thin-film metal electrode.

25 12. An electron emission device comprising:

an electron supply layer of metal or semiconductor;  
 an insulator layer formed on the electron supply layer; and  
 a thin-film metal electrode formed on the insulator layer and facing a vacuum space,  
 30 characterized in that said insulator layer is made of silicon oxide as a major component of  $\text{SiO}_x$  where x represents an atomic ratio satisfying a refractive index in a range of from 1.3 to 3.0 and has a film thickness of  
 50 nm or greater, whereby the electron emission device emits electrons when an electric field is applied between  
 the electron supply layer and the thin-film metal electrode.

35 13. An electron emission display device comprising:

a pair of first and second substrates facing each other with a vacuum space in between;  
 a plurality of electron emission devices provided on the first substrate;  
 a collector electrode provided in the second substrate; and  
 40 a fluorescent layer formed on the collector electrode,  
 each of the electron emission devices comprising an electron supply layer of metal or semiconductor; an  
 insulator layer formed on the electron supply layer; and a thin-film metal electrode formed on the insulator  
 layer and facing a vacuum space, wherein said insulator layer is made of silicon oxide as a major component  
 of  $\text{SiO}_x$  where x represents an atomic ratio in a range of from 0.5 to 2 in a whole layer and has a film thickness  
 45 of 50 nm or greater.

14. An electron emission display device comprising:

a pair of a device substrate and a transparent substrate facing each other with a vacuum space in between;  
 50 a plurality of ohmic electrodes formed in parallel on an inner surface of the device substrate;  
 a plurality of electron emission devices provided on the ohmic electrodes, each of the electron emission devices  
 comprising an electron supply layer of metal or semiconductor; an insulator layer formed on the electron supply  
 layer; and a thin-film metal electrode formed on the insulator layer and facing the vacuum space, wherein said  
 insulator layer is made of silicon oxide as a major component of  $\text{SiO}_x$  where x represents an atomic ratio in a  
 55 range of from 0.5 to 2 in a whole layer and has a film thickness of 50 nm or greater;  
 a plurality of bus electrodes formed on parts of the thin-film metal electrodes and extending in parallel to one  
 another and perpendicular to the ohmic electrodes so as to electrically connect adjoining thin-film metal electrodes;

a plurality of collector electrodes provided in the transparent substrate; and  
fluorescent layers formed on the collector electrodes.

15. An electron emission display device according to claim 14 further comprising a second auxiliary insulator layer with openings formed to separate the surfaces of the thin-film metal electrodes into a plurality of electron emission regions.

16. An electron emission display device according to claim 15, wherein the second auxiliary insulator layer covers the bus electrodes.

17. An electron emission device comprising:

an electron supply layer of metal or semiconductor;  
an insulator layer formed on the electron supply layer; and  
a thin-film metal electrode formed on the insulator layer and facing a vacuum space,  
characterized in that one of said thin-film metal electrode and said insulator layer is provided with a region containing atomic elements each having a work function which is lower than that of said thin-film metal electrode, whereby the electron emission device emits electrons when an electric field is applied between the electron supply layer and the thin-film metal electrode.

18. An electron emission display device according to claim 17, wherein said region is an intermediate layer disposed between said thin-film metal electrode and said insulator layer.

19. An electron emission display device according to claim 17, wherein said region is an electron emission layer disposed on an outer surface of said thin-film metal electrode at a side of electron emission.

20. An electron emission display device according to claim 17, wherein said region is disposed so as to be dispersed as a layer within said thin-film metal electrode.

21. An electron emission display device according to claim 17, wherein said region is disposed so as to be dispersed as a layer within said insulator layer.

22. An electron emission display device comprising:

a pair of first and second substrates facing each other with a vacuum space in between;  
a plurality of electron emission devices provided on the first substrate;  
a collector electrode provided in the second substrate; and  
a fluorescent layer formed on the collector electrode,  
each of the electron emission devices comprising an electron supply layer of metal or semiconductor;  
an insulator layer formed on the electron supply layer; and a thin-film metal electrode formed on the insulator layer and facing a vacuum space, wherein one of said thin-film metal electrode and said insulator layer is provided with a region containing atomic elements each having a work function which is lower than that of said thin-film metal electrode.

23. An electron emission device comprising:

an electron supply layer of metal or semiconductor;  
an insulator layer formed on the electron supply layer; and  
a thin-film metal electrode formed on the insulator layer and facing a vacuum space,  
characterized in that said insulator layer has a film thickness of 50 nm or greater and is provided with one or more thermal conductive layers, whereby the electron emission device emits electrons when an electric field is applied between the electron supply layer and the thin-film metal electrode.

24. An electron emission display device comprising:

a pair of first and second substrates facing each other with a vacuum space in between;  
a plurality of electron emission devices provided on the first substrate;  
a collector electrode provided in the second substrate; and

a fluoroscent layer formed on the collector electrode,  
 each of the electron emission devices comprising an electron supply layer of metal or semiconductor; an  
 insulator layer formed on the electron supply layer; and a thin-film metal electrode formed on the insulator  
 layer and facing a vacuum space, wherein said insulator layer has a film thickness of 50 nm or greater and is  
 provided with one or more thermal conductive layers.

**25. An electron emission device comprising:**

an electron supply layer of metal or semiconductor;  
 an insulator layer formed on the electron supply layer; and  
 a thin-film metal electrode formed on the insulator layer and facing a vacuum space,  
 characterized in that said insulator layer is made of a dielectric substance and has a film thickness of 50 nm  
 or greater which is formed through a sputtering by using a mixture gas essentially comprising a rare gas under  
 a sputtering condition of a gas pressure of 0.1 to 100 mTorr and a forming rate of 0.5 to 100 nm/min, whereby  
 the electron emission device emits electrons when an electric field is applied between the electron supply  
 layer and the thin-film metal electrode.

**26. An electron emission display device comprising:**

a pair of first and second substrates facing each other with a vacuum space in between;  
 a plurality of electron emission devices provided on the first substrate;  
 a collector electrode provided in the second substrate; and  
 a fluorescent layer formed on the collector electrode,  
 each of the electron emission devices comprising an electron supply layer of metal or semiconductor; an  
 insulator layer formed on the electron supply layer; and a thin-film metal electrode formed on the insulator  
 layer and facing a vacuum space, wherein said insulator layer is made of a dielectric substance and has a  
 film thickness of 50 nm or greater which is formed through a sputtering by using a mixture gas essentially  
 comprising a rare gas under a sputtering condition of a gas pressure of 0.1 to 100 mTorr and a forming rate  
 of 0.5 to 100 nm/min.

**27. An electron emission display device comprising:**

a pair of a device substrate and a transparent substrate facing each other with a vacuum space in between;  
 a plurality of ohmic electrodes formed in parallel on an inner surface of the device substrate;  
 a plurality of electron emission devices provided on the ohmic electrodes, each of the electron emission devices  
 comprising an electron supply layer of metal or semiconductor; an insulator layer formed on the electron supply  
 layer; and a thin-film metal electrode formed on the insulator layer and facing the vacuum space, wherein said  
 insulator layer is made of a dielectric substance and has a film thickness of 50 nm or greater which is formed  
 through a sputtering by using a mixture gas essentially comprising a rare gas under a sputtering condition of  
 a gas pressure of 0.1 to 100 mTorr and a forming rate of 0.5 to 100 nm/min;  
 a plurality of bus electrodes formed on parts of the thin-film metal electrodes and extending in parallel to one  
 another and perpendicular to the ohmic electrodes so as to electrically connect adjoining thin-film metal elec-  
 trodes;  
 a plurality of collector electrodes provided in the transparent substrate; and  
 fluorescent layers formed on the collector electrodes.

**28. An electron emission display device according to claim 27 further comprising a second auxiliary insulator layer  
 with openings formed to separate the surfaces of the thin-film metal electrodes into a plurality of electron emission  
 regions.**

**29. An electron emission display device according to claim 28, wherein the second auxiliary insulator layer covers the  
 bus electrodes.**

**30. An electron emission device comprising:**

an electron supply layer of metal or semiconductor;  
 an insulator layer formed on the electron supply layer; and  
 a thin-film metal electrode formed on the insulator layer and facing a vacuum space,

characterized in that said insulator layer has a smoothed surface layer for contacting with said thin-film metal electrode, whereby the electron emission device emits electrons when an electric field is applied between the electron supply layer and the thin-film metal electrode.

- 5 31. An electron emission device according to claim 30, wherein said insulator layer is made of a dielectric substance and has a film thickness of 50 nm or greater which is formed through a sputtering method under a sputtering condition in which a gas pressure or a forming rate for said surface layer of said insulator layer is lower than that for a portion other than said surface layer of said insulator layer.
- 10 32. An electron emission device according to claim 30, wherein said insulator layer is made of a dielectric substance and has a film thickness of 50 nm or greater which is formed through a sputtering method by using a mixture gas essentially comprising a rare gas in such a manner that a portion other than said surface layer of said insulator layer is deposited under a sputtering condition of a gas pressure of 2 to 100 mTorr and a forming rate of 0.1 to 100 nm/min before said surface layer of said insulator layer is deposited under a sputtering condition of a gas pressure of 0.1 to 1 mTorr and a forming rate of 0.1 to 100 nm/min.
- 15 33. An electron emission device according to claim 30, wherein said insulator layer is made of a dielectric substance and has a film thickness of 50 nm or greater which is formed through a sputtering method by using a mixture gas essentially comprising a rare gas in such a manner that a portion other than said surface layer of said insulator layer is deposited under a sputtering condition of a gas pressure of 0.1 to 100 mTorr and a forming rate of 20 to 100 nm/min before said surface layer of said insulator layer is deposited under a sputtering condition of a gas pressure of 0.1 to 100 mTorr and a forming rate of 0.1 to 10 nm/min.
- 20 34. An electron emission device according to claim 33, wherein said mixture gas contains xenon or krypton.
- 25 35. An electron emission device according to claim 30, wherein an electric resistance of said surface layer for contacting with said thin-film metal electrode is higher than that of a portion other than said surface layer of said insulator layer.
- 30 36. An electron emission device according to claim 30, wherein an electric resistance of said surface layer for contacting with said thin-film metal electrode is smoothed by a sputter-etching.
37. An electron emission device according to claim 30, wherein said electron supply layer is made of silicon and said insulator is made of silicon oxide.
- 35 38. An electron emission display device comprising:
  - a pair of first and second substrates facing each other with a vacuum space in between;
  - a plurality of electron emission devices provided on the first substrate;
  - a collector electrode provided in the second substrate; and
  - 40 a fluorescent layer formed on the collector electrode,
  - each of the electron emission devices comprising an electron supply layer of metal or semiconductor; an insulator layer formed on the electron supply layer; and a thin-film metal electrode formed on the insulator layer and facing a vacuum space, wherein said insulator layer has a smoothed surface layer for contacting with said thin-film metal electrode.
  - 45
39. An electron emission device according to claim 34, wherein said mixture gas contains xenon or krypton.

50

55

FIG. 1

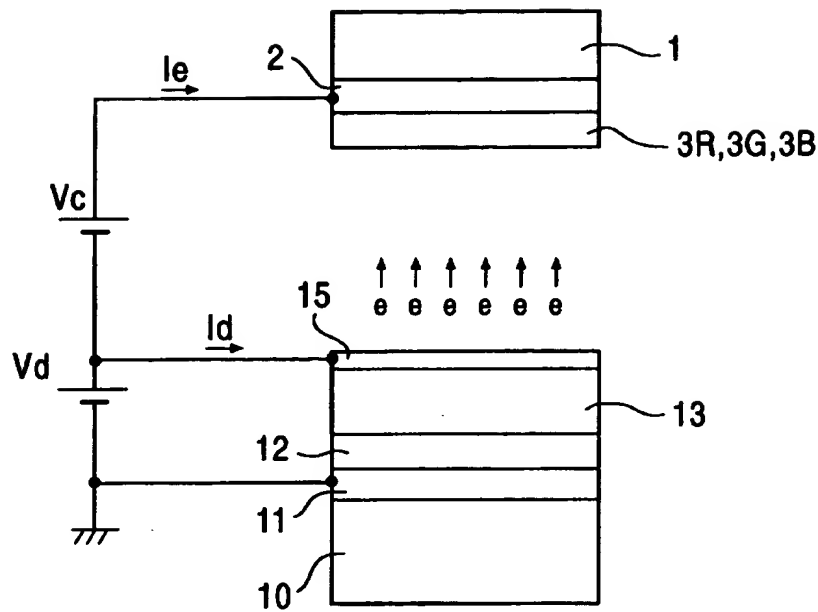


FIG. 2

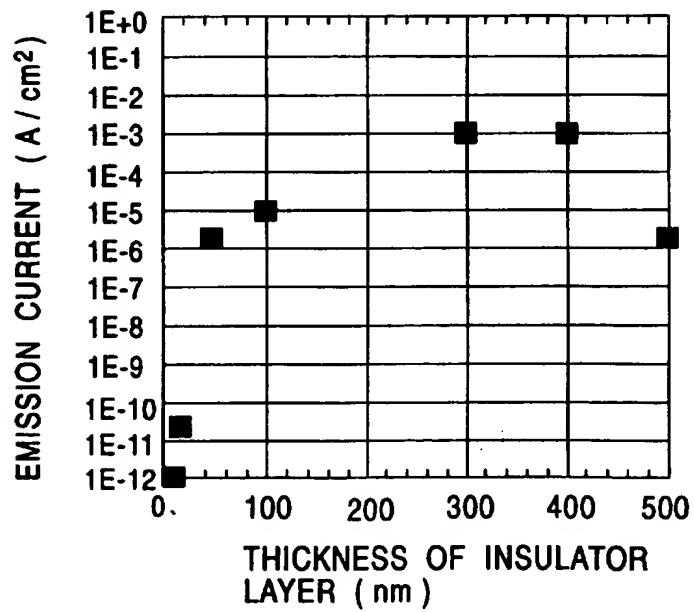


FIG. 3

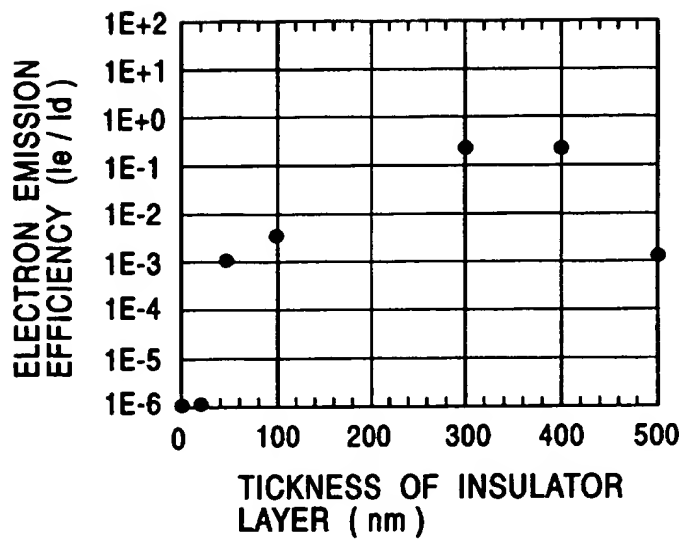


FIG. 4

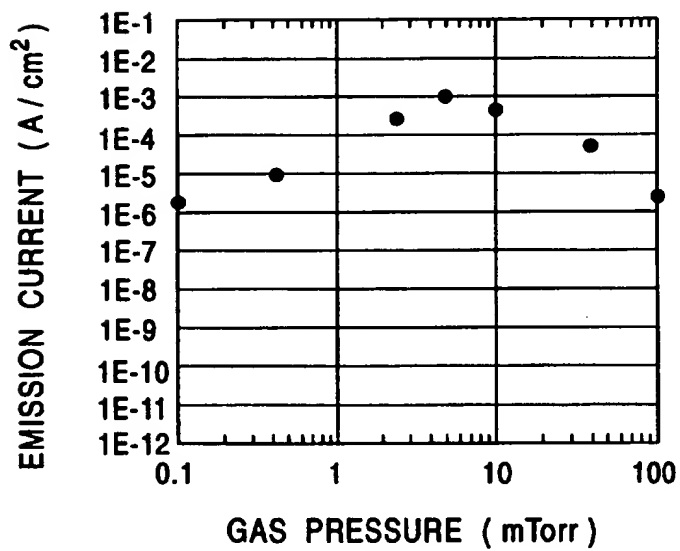


FIG. 5

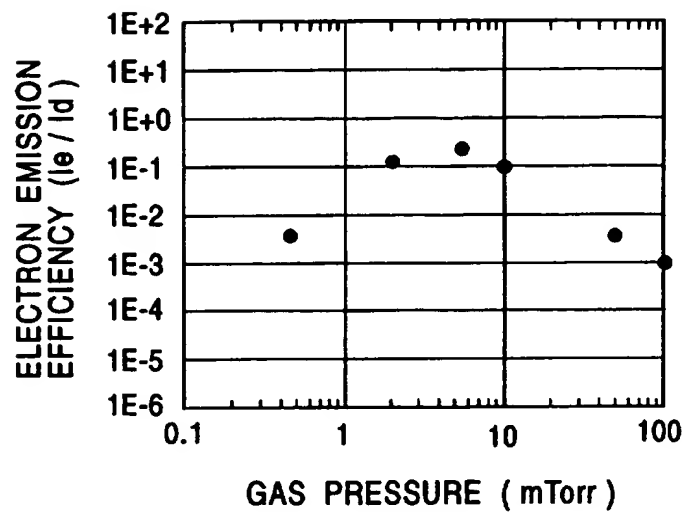


FIG. 6

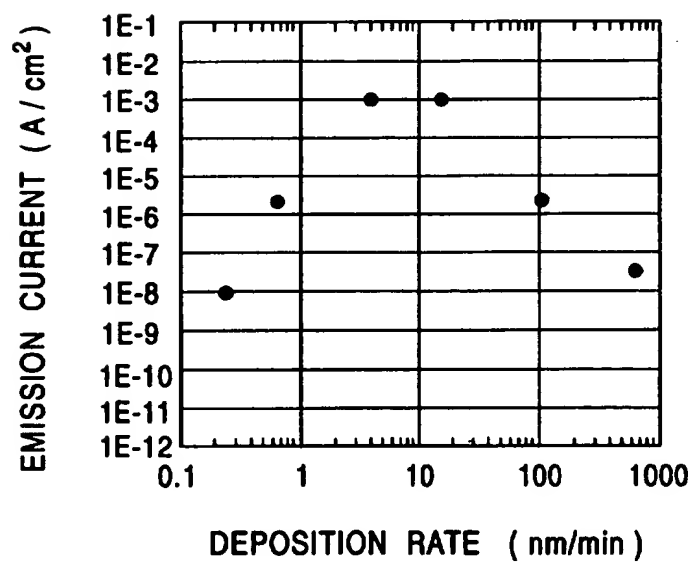


FIG. 7

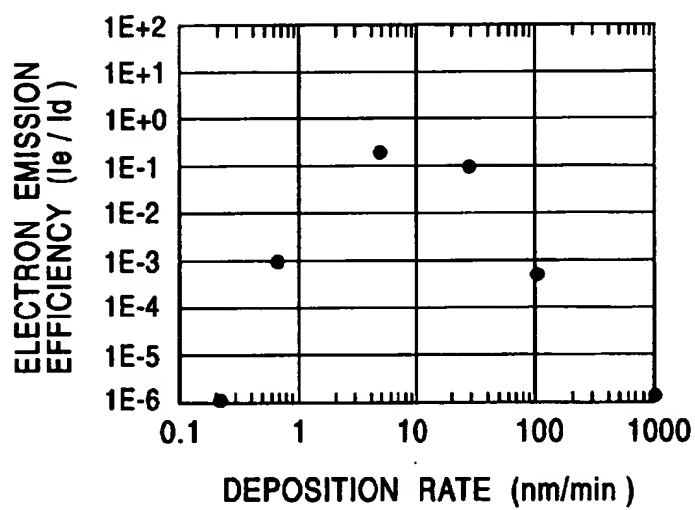




FIG. 8

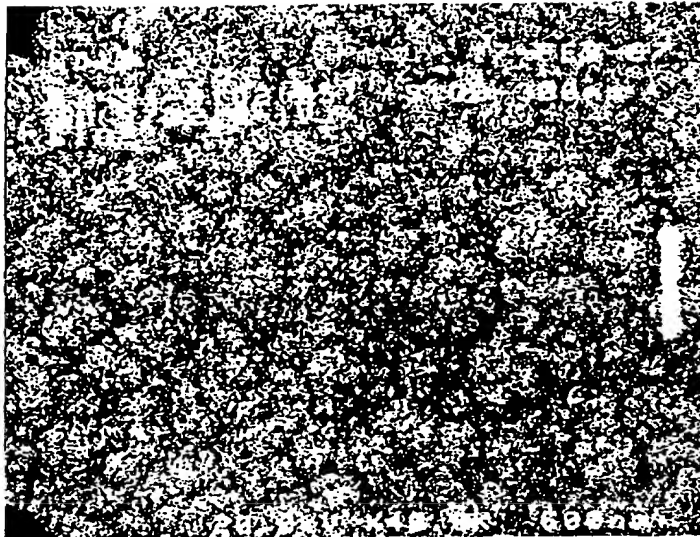


FIG. 9

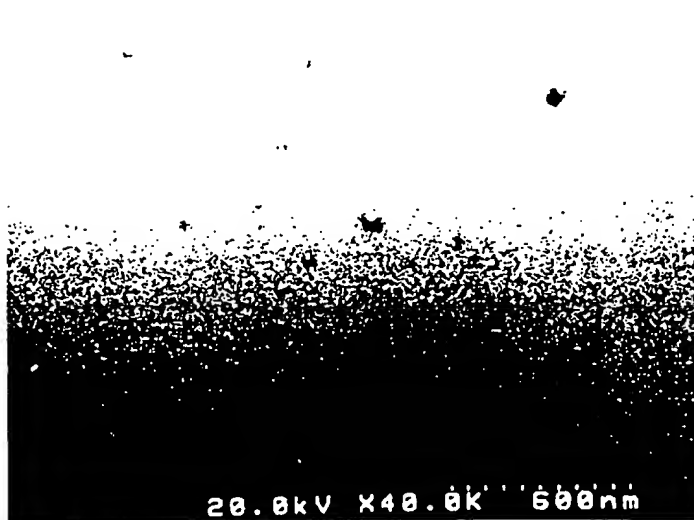


FIG. 10

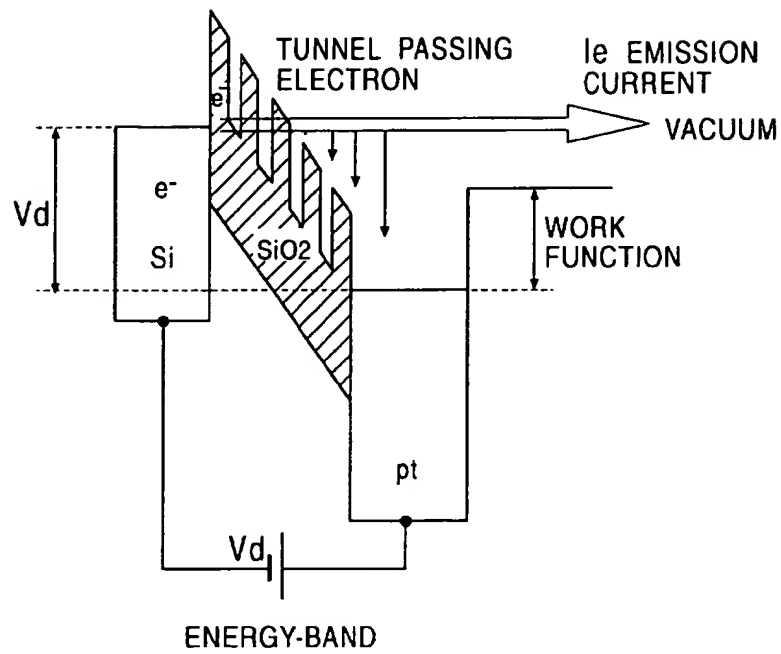


FIG. 11

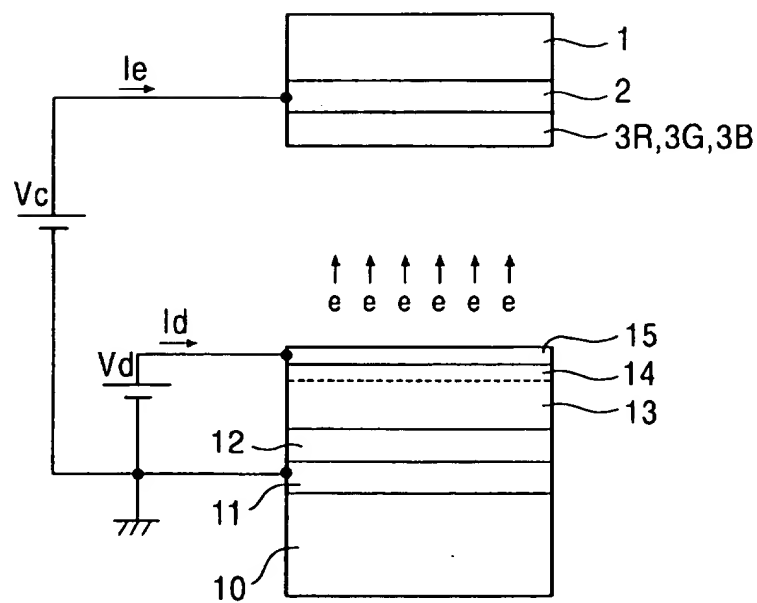


FIG. 12

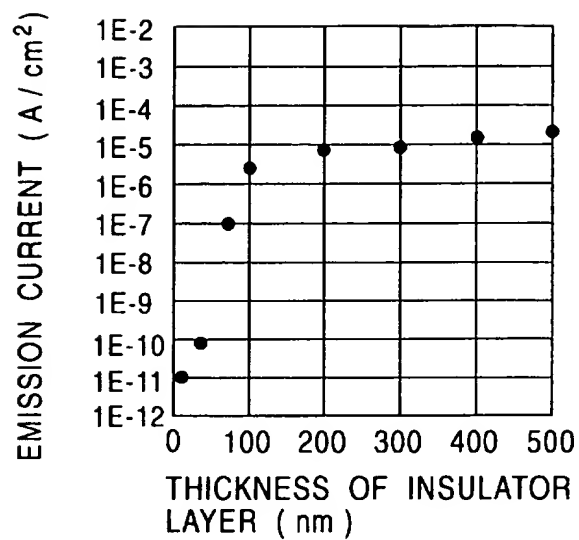


FIG. 13

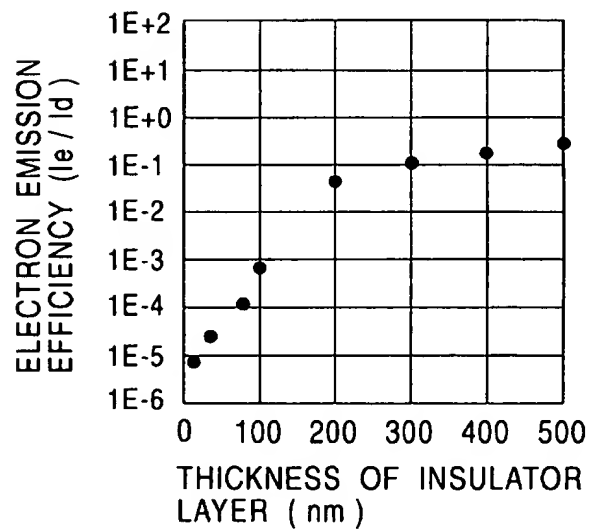


FIG. 14

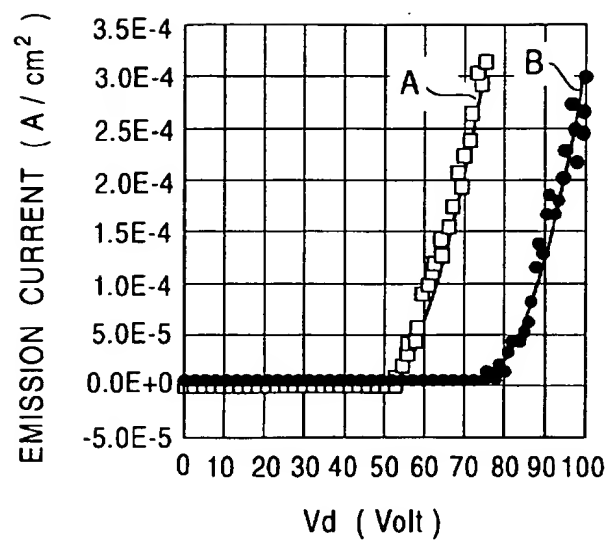


FIG. 15A

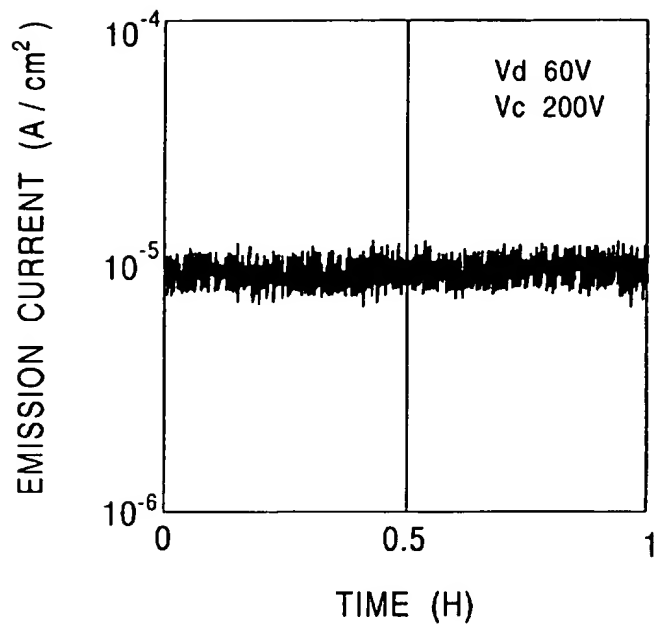


FIG. 15B

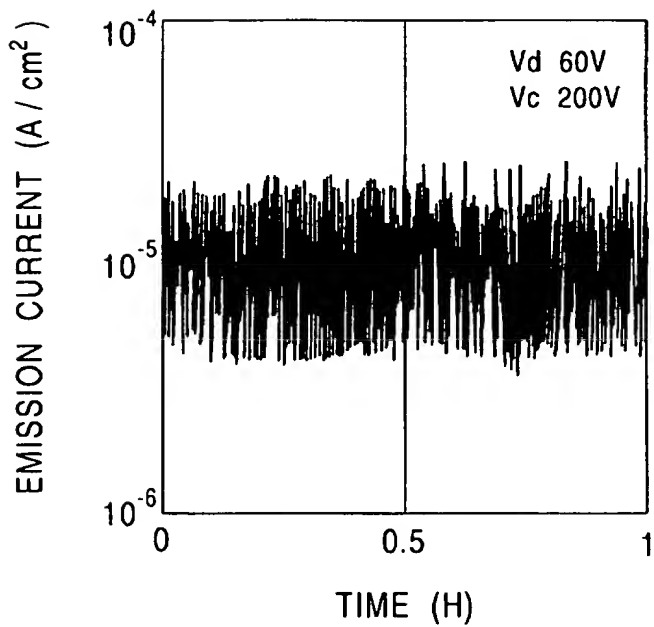


FIG. 16

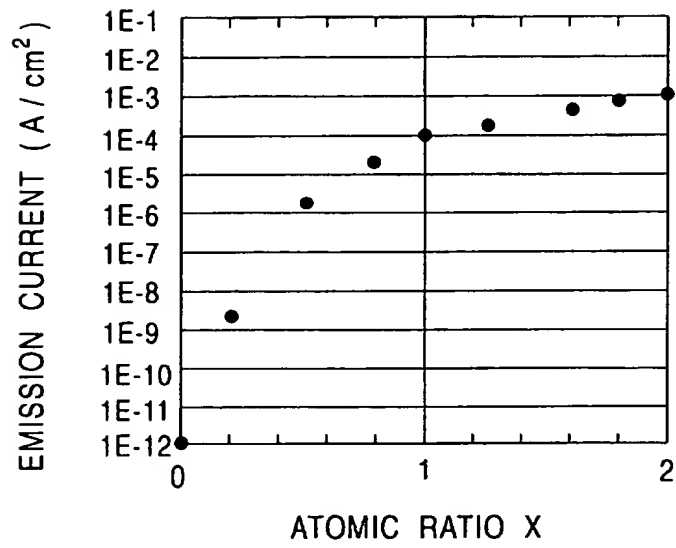


FIG. 17

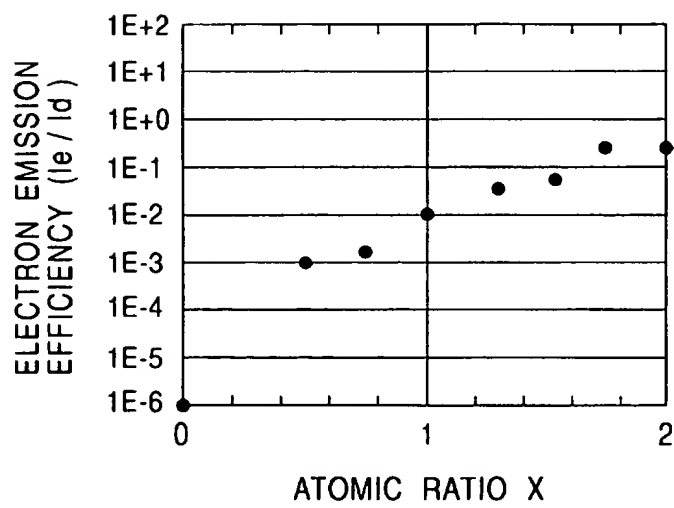


FIG. 18

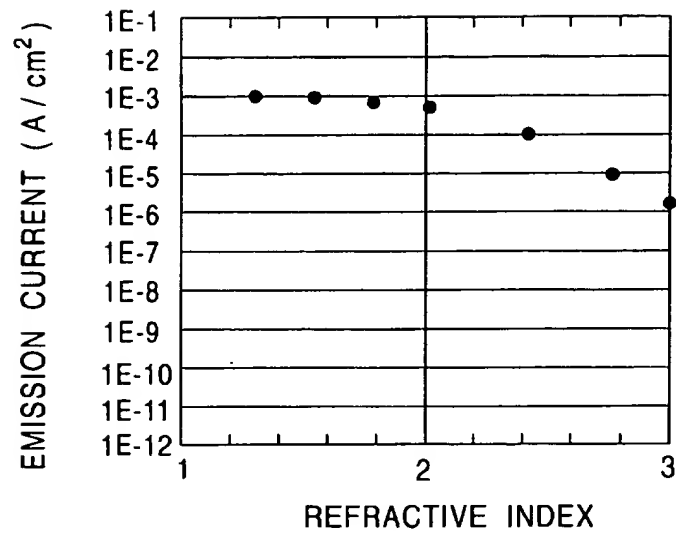


FIG. 19

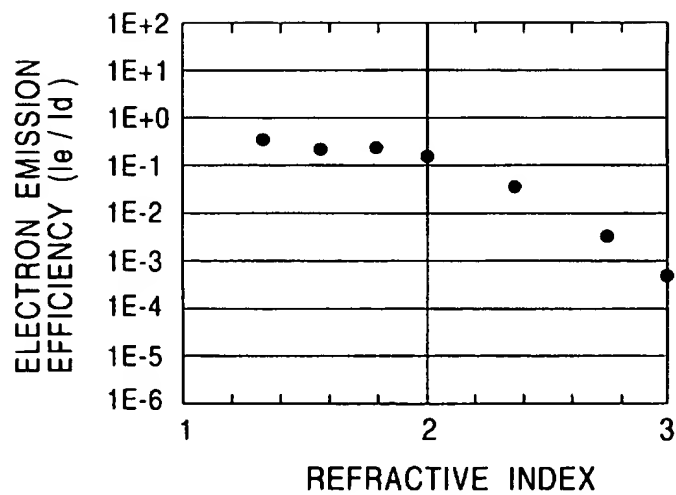


FIG. 20

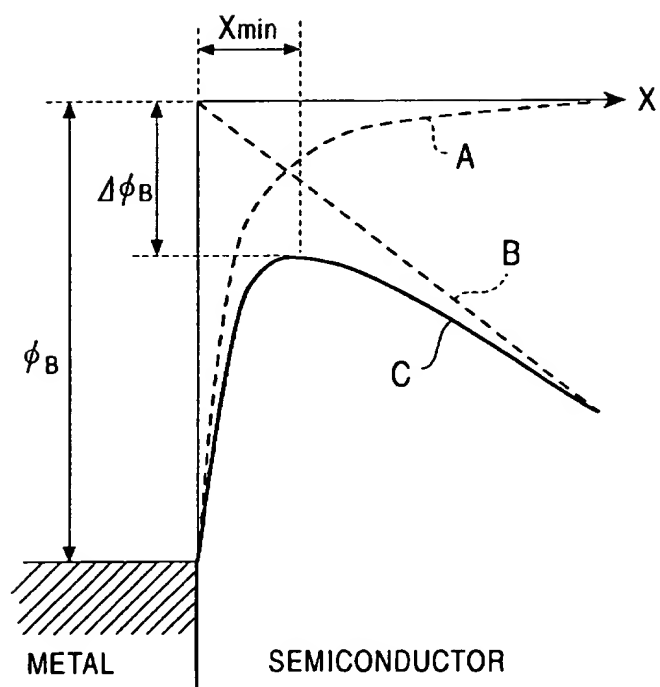




FIG. 21

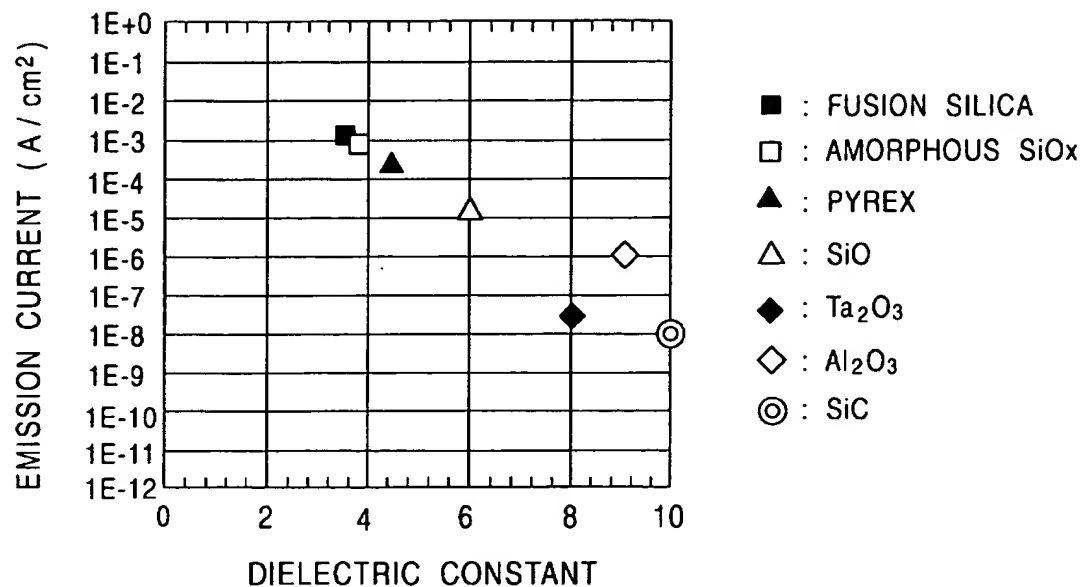


FIG. 22

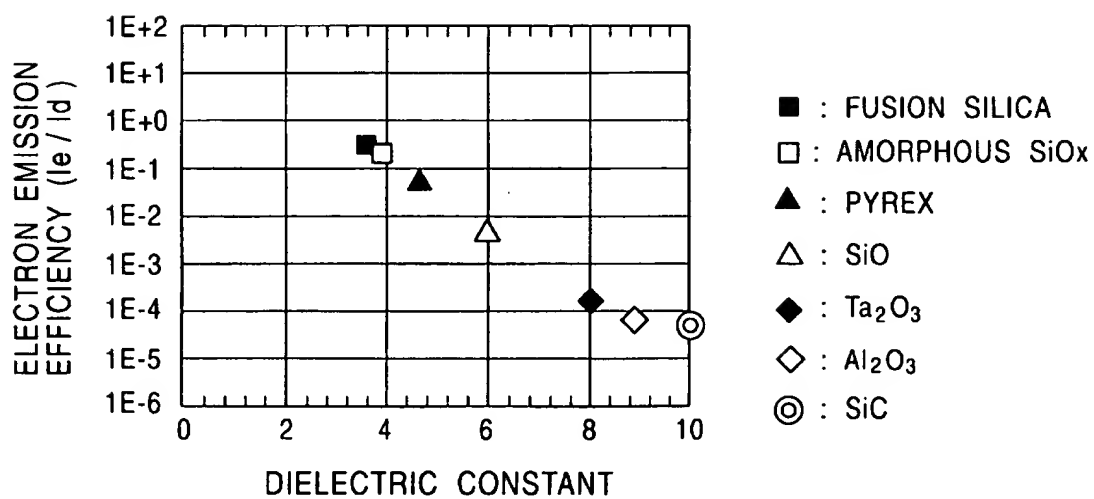


FIG. 23

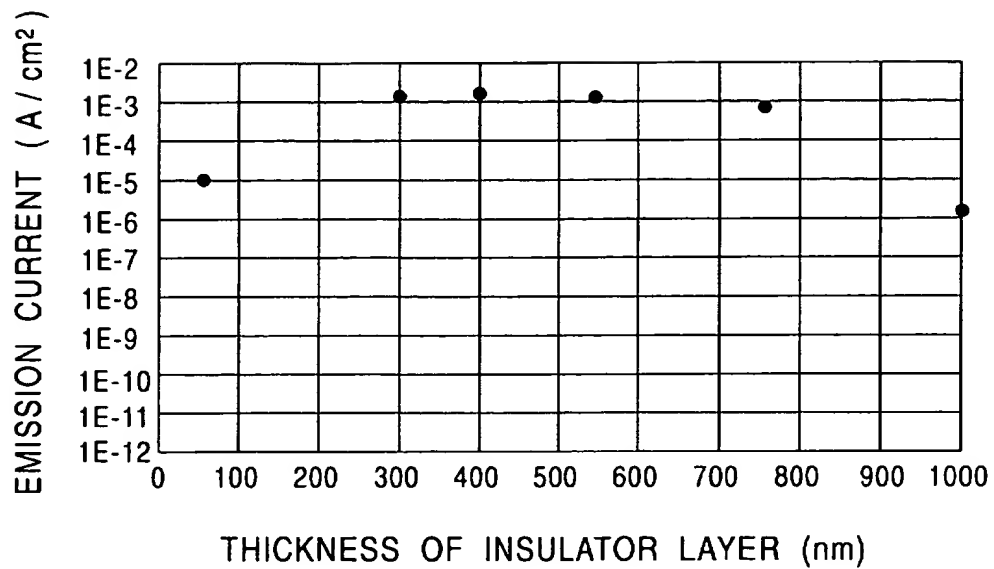


FIG. 24

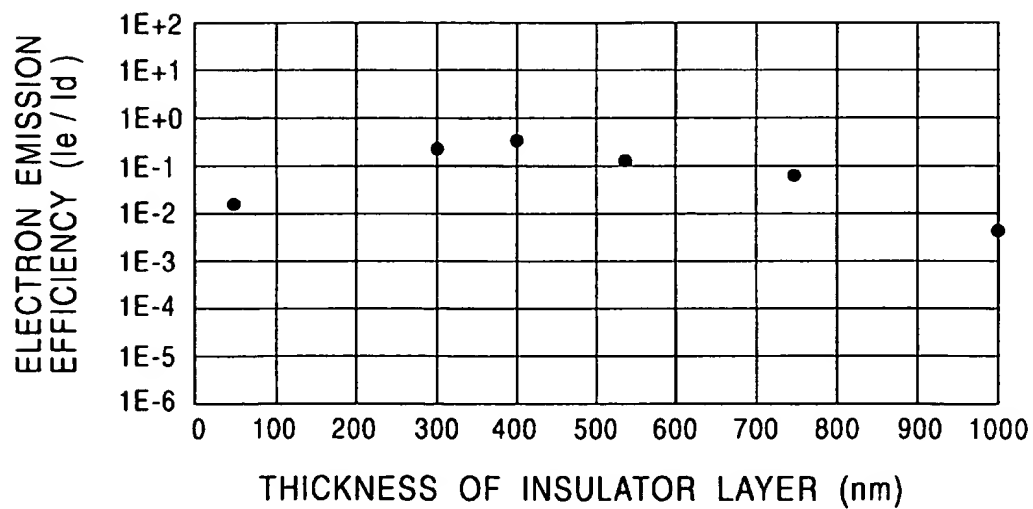


FIG. 25

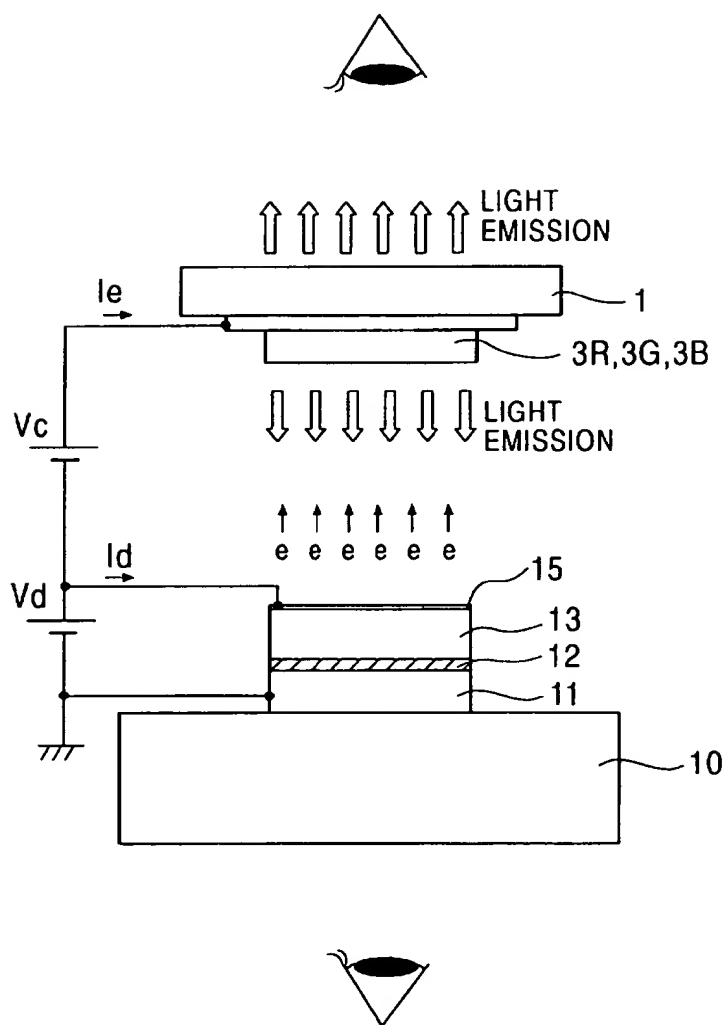


FIG. 26

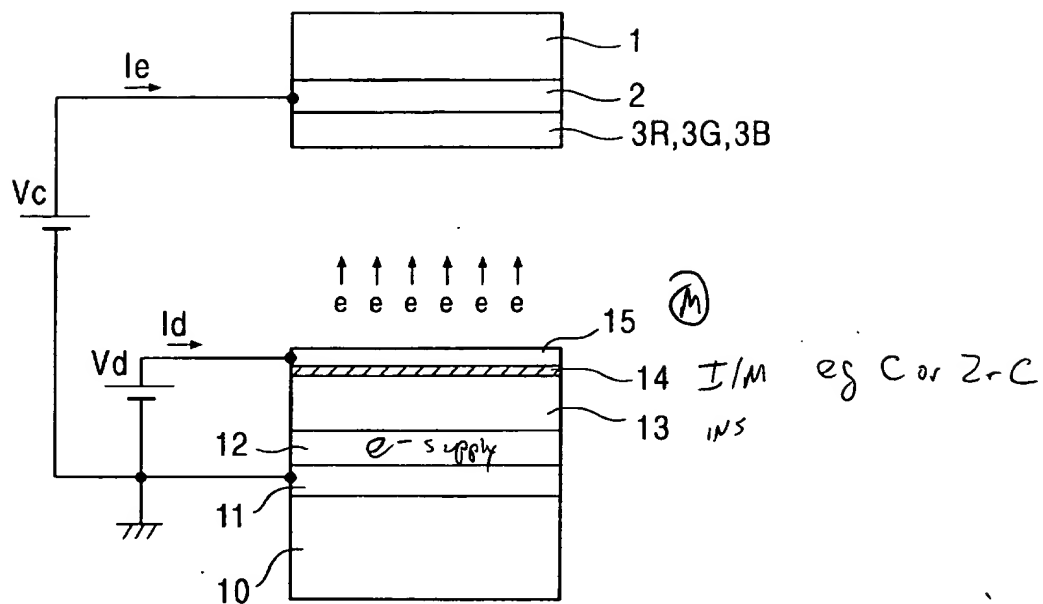


FIG. 27

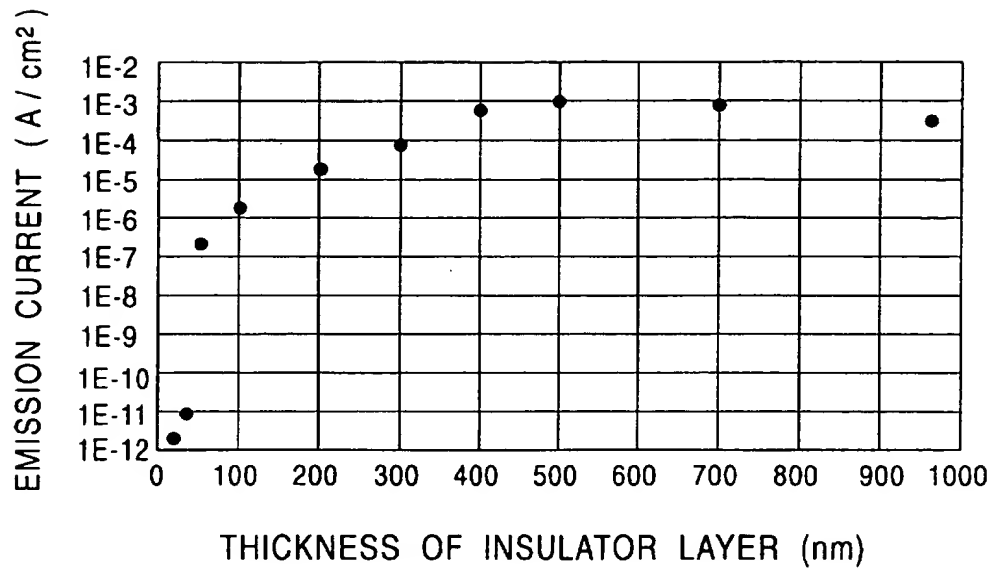


FIG. 28

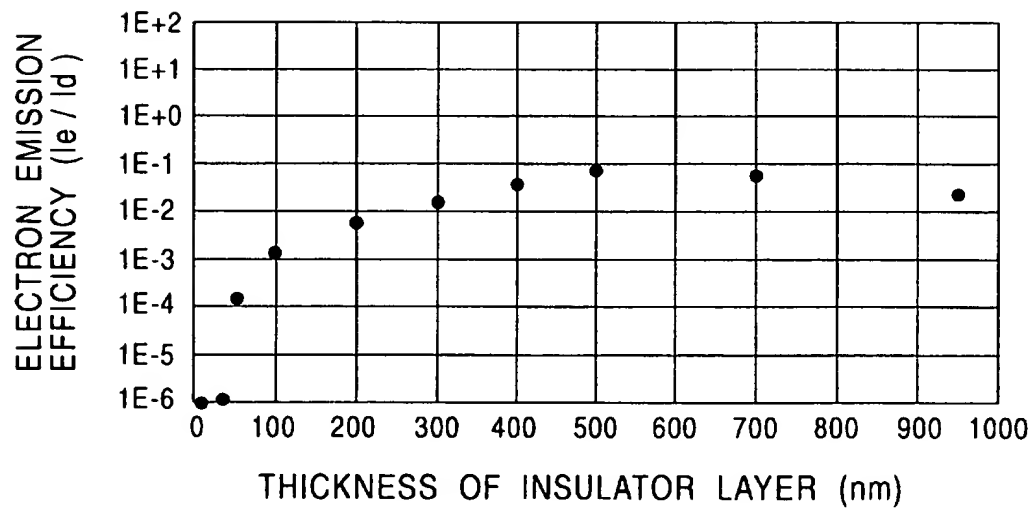


FIG. 29

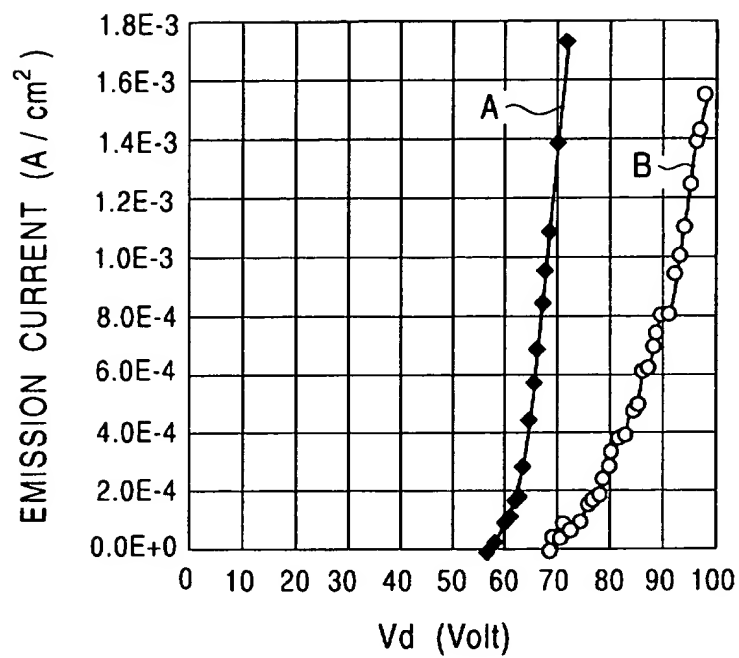


FIG. 30

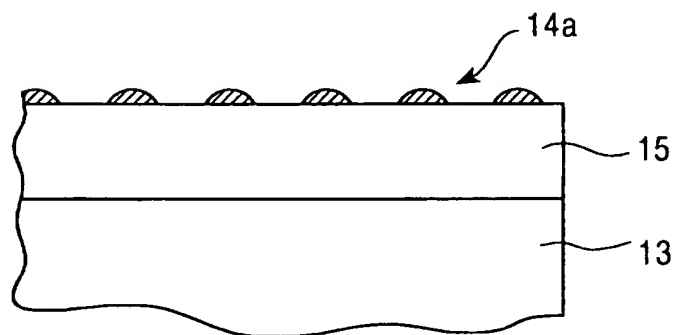


FIG. 31

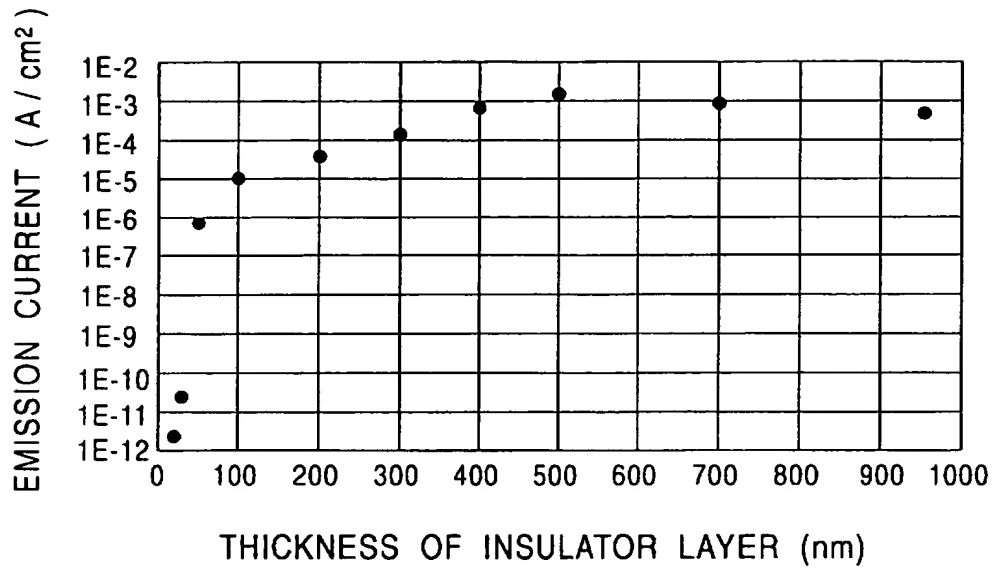


FIG. 32

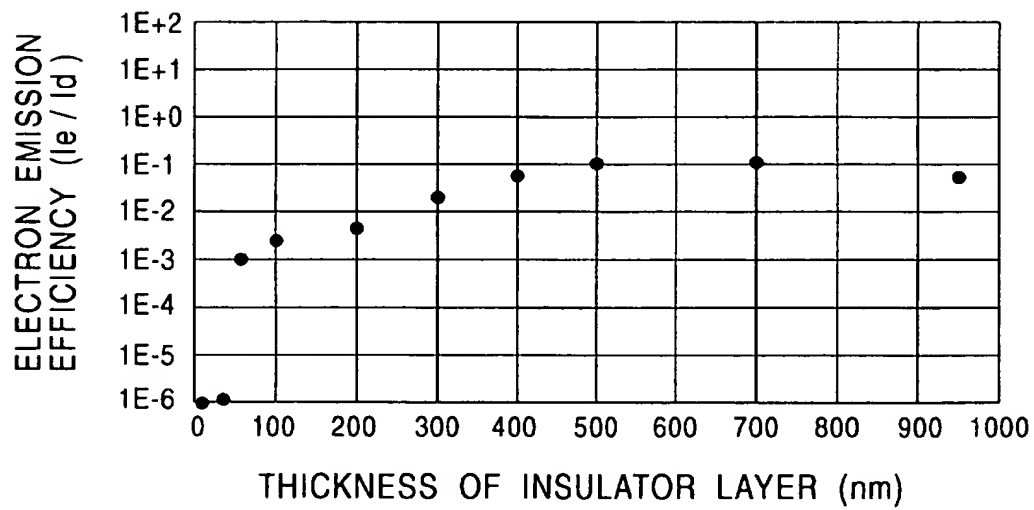


FIG. 33

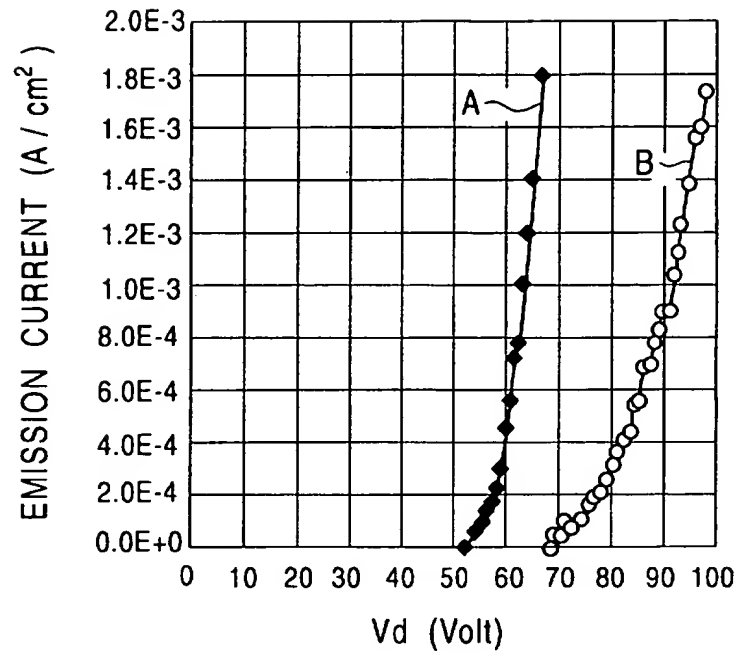


FIG. 34

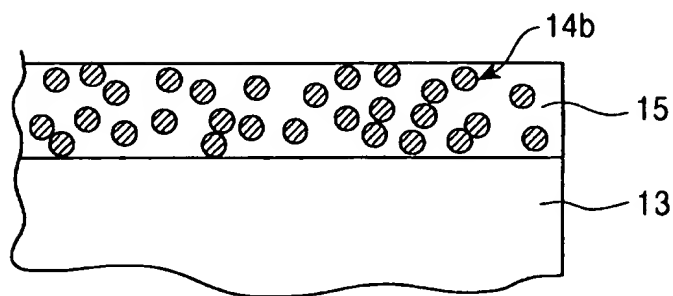




FIG. 35

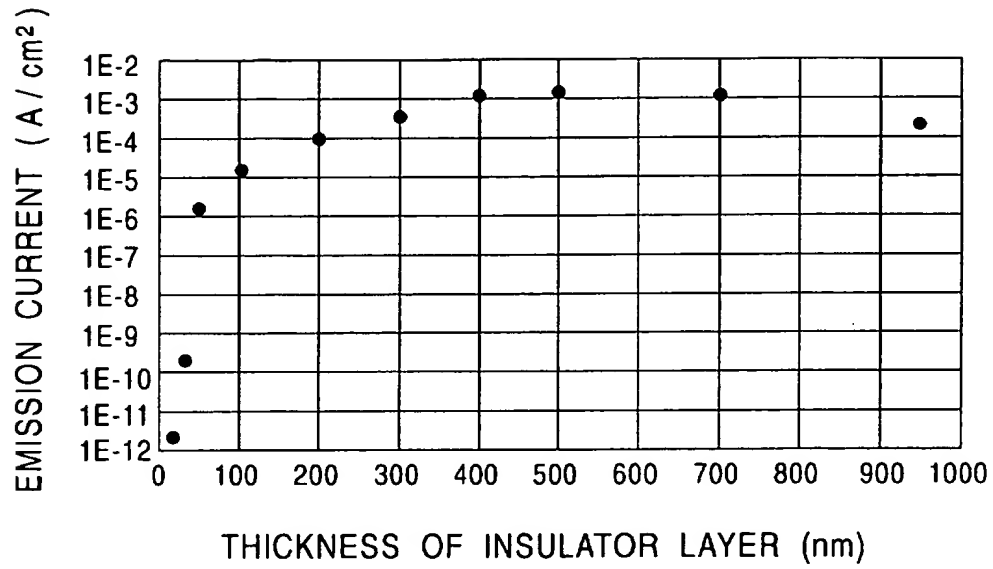


FIG. 36

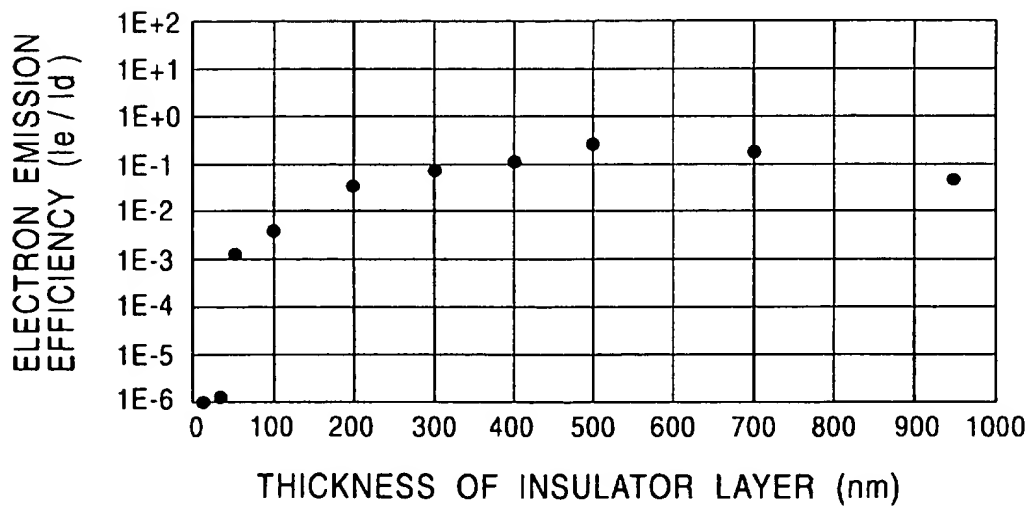


FIG. 37

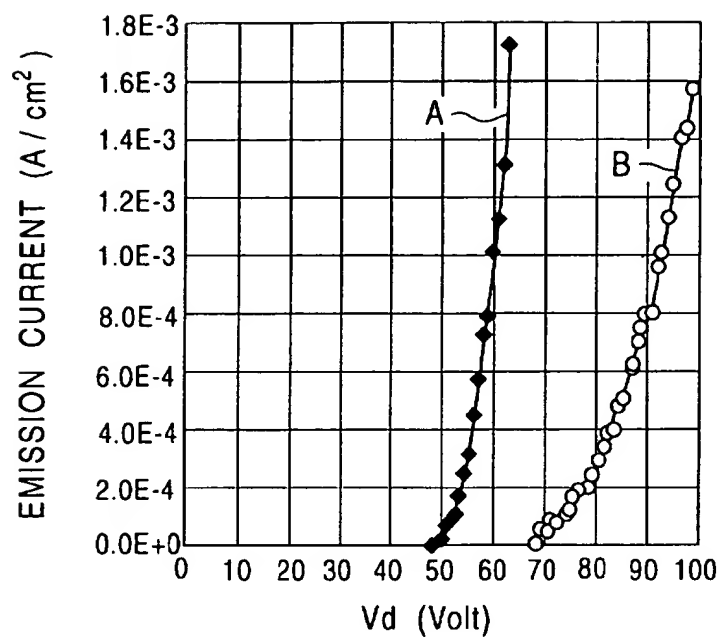


FIG. 38

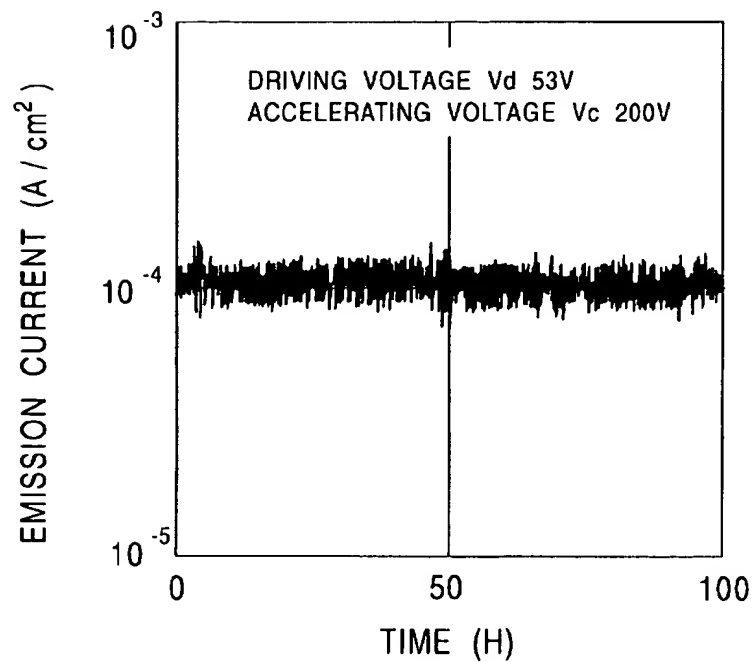


FIG. 39

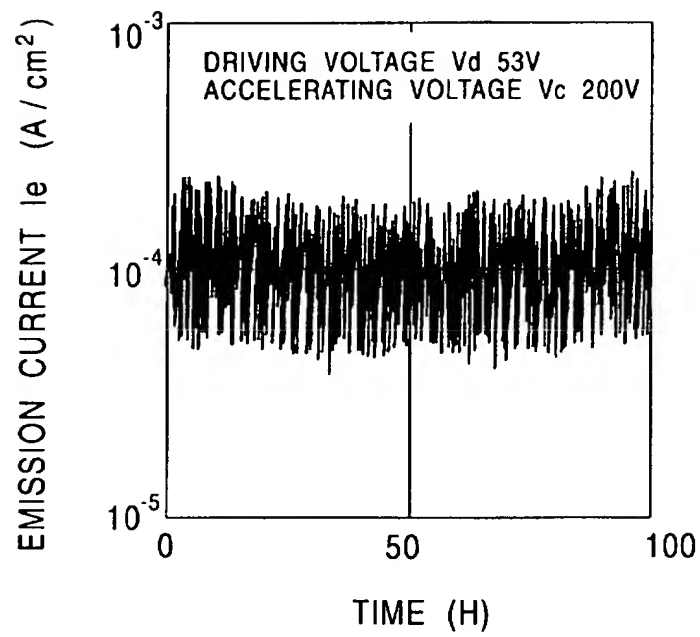


FIG. 40

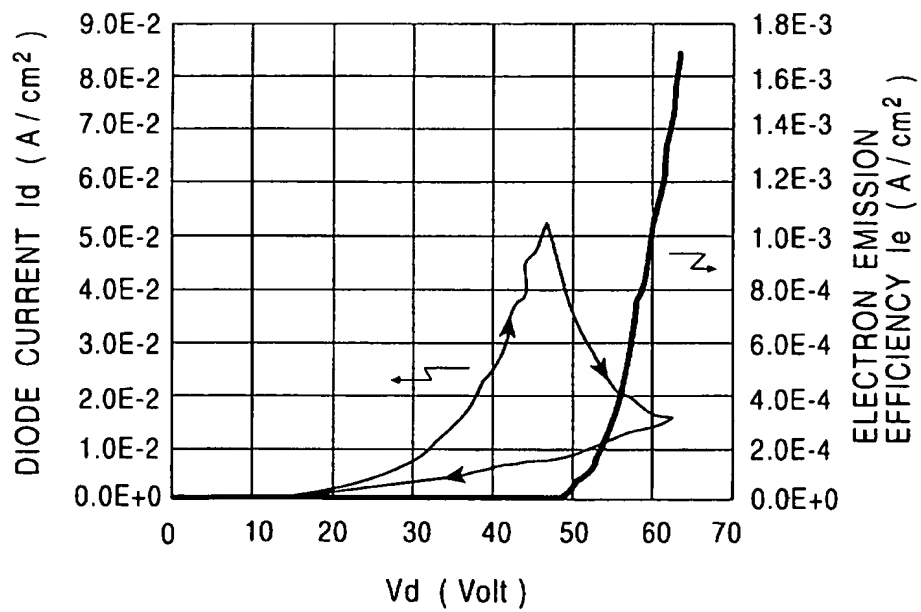


FIG. 41

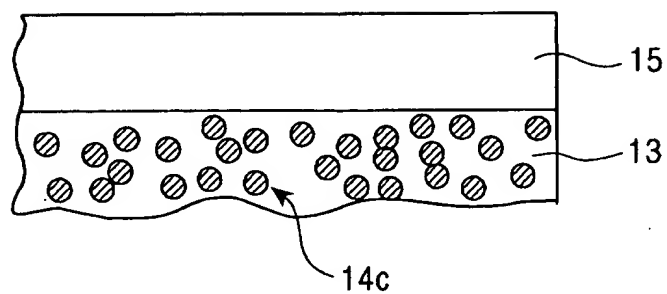


FIG. 42

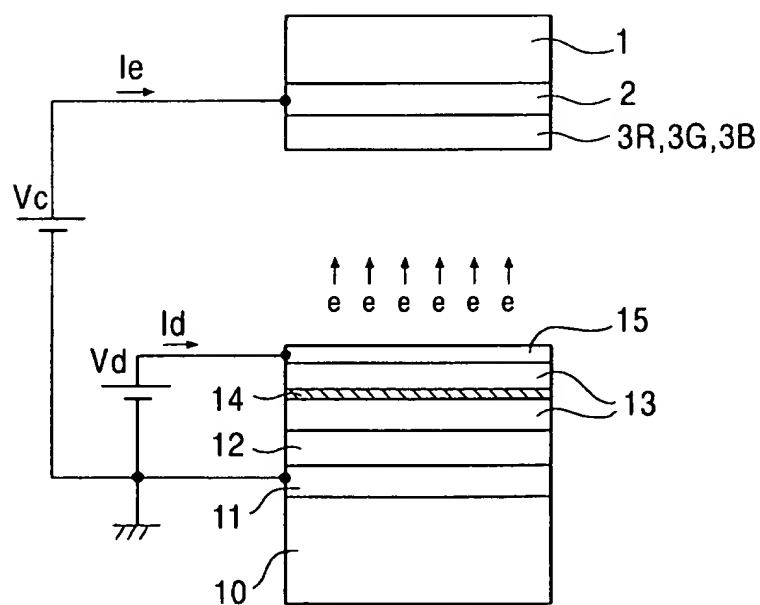


FIG. 43

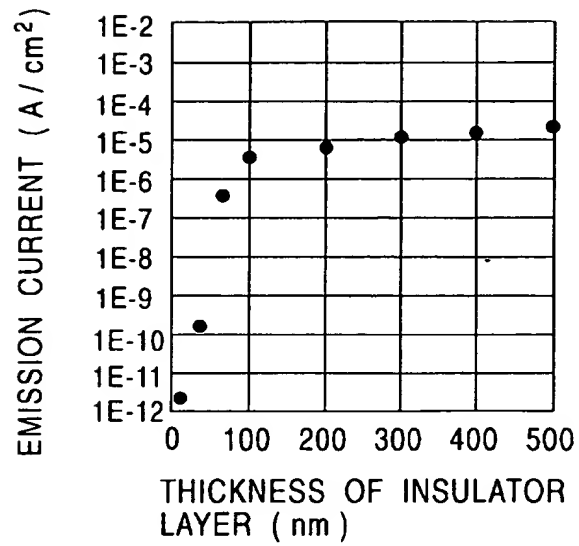


FIG. 44

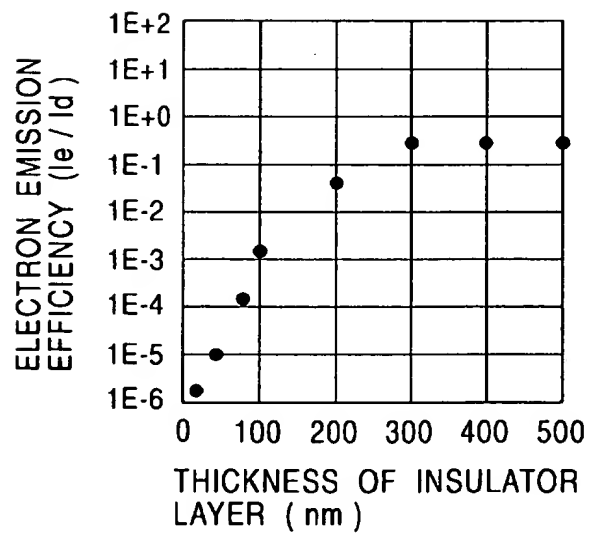


FIG. 45

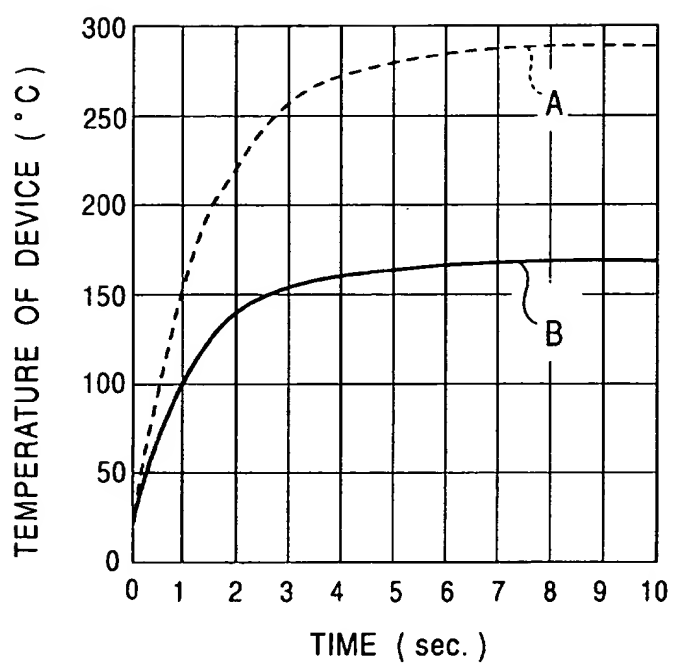


FIG. 46A

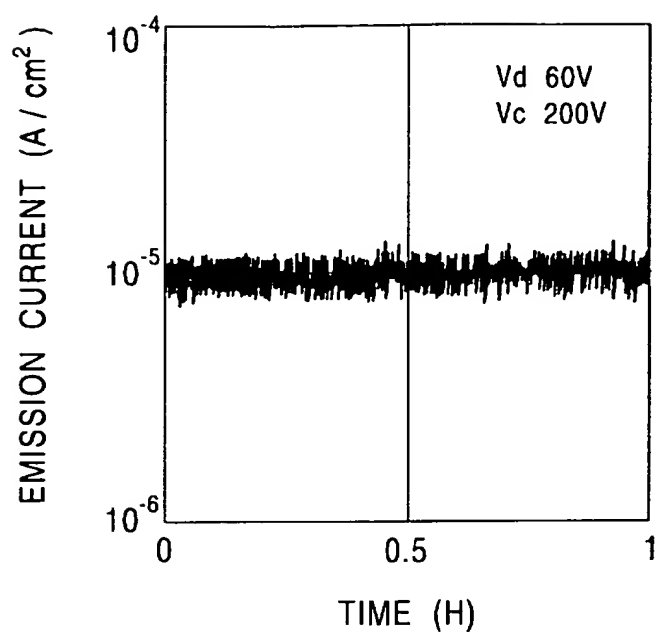


FIG. 46B

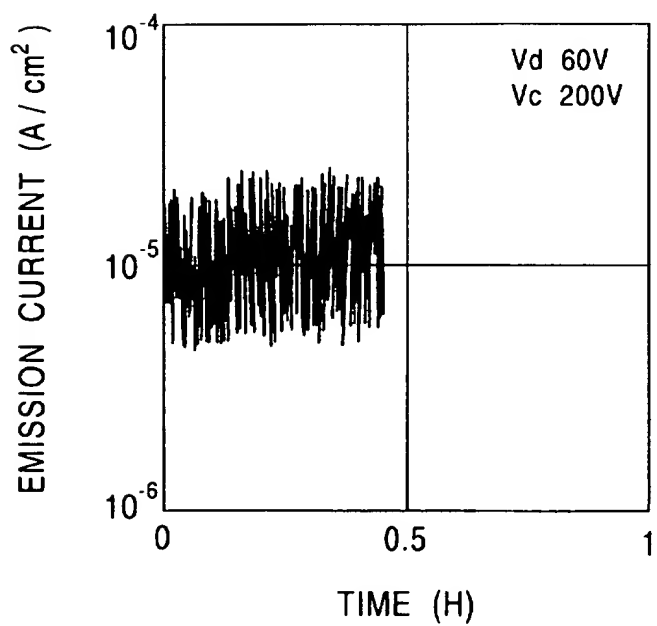
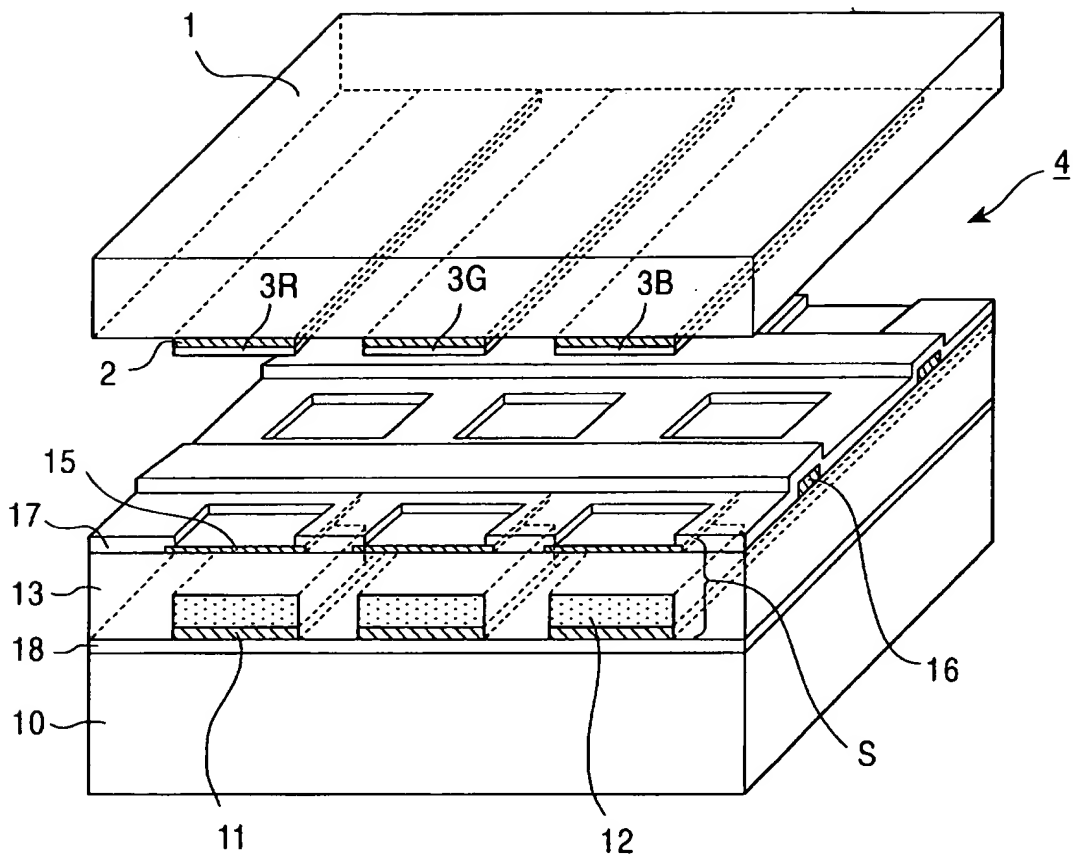


FIG. 47







European Patent  
Office

# EUROPEAN SEARCH REPORT

Application Number  
EP 98 30 1594

DOCUMENTS CONSIDERED TO BE RELEVANT			
Category	Citation of document with indication, where appropriate, of relevant passages	Relevant to claim	CLASSIFICATION OF THE APPLICATION (Int.Cl.6)
D,X	PATENT ABSTRACTS OF JAPAN vol. 095, no. 006, 31 July 1995 & JP 07 065710 A (HITACHI LTD), 10 March 1995, * abstract *	1,6,8, 11,12, 23,25	H01J1/30
X	TROYAN P E: "FLAT DISPLAY BASED ON THE METAL-INSULATOR-METAL EMITTER ARRAY" JOURNAL OF VACUUM SCIENCE AND TECHNOLOGY: PART 8, vol. 11, no. 2, 1 March 1993, pages 514-517, XP000364858 * the whole document *	1-16,23, 25-29,38	
P,X	EP 0 798 761 A (PIONEER ELECTRONIC CORP ;KOSHIDA NOBUYOSHI (JP)) 1 October 1997 * the whole document *	1-16,23, 25-29,38	
X	R R VERDERBER AND J G SIMMONS: "A hot electron, cold cathode emitter" THE RADIO AND ELECTRONIC ENGINEER, vol. 33, June 1967, pages 347-351, XP002067029 * page 347 - page 348 *	1	
X	ECKERTOVA L: "METAL-INSULATOR-METAL AND METAL-INSULATOR STRUCTURES AS ELECTRON SOURCES" INTERNATIONAL JOURNAL OF ELECTRONICS, vol. 69, no. 1, 1 July 1990, pages 65-78, XP000138363 * the whole document *	1	
X	US 3 706 920 A (CALDWELL LARRY V ET AL) 19 December 1972 * figure 2 * * column 2 - column 3 *	17-19	TECHNICAL FIELDS SEARCHED (Int.Cl.6) H01J
The present search report has been drawn up for all claims			
Place of search THE HAGUE		Date of completion of the search 4 June 1998	Examiner Colvin, G
<p>CATEGORY OF CITED DOCUMENTS</p> <p>X : particularly relevant if taken alone Y : particularly relevant if combined with another document of the same category A : technological background O : non-written disclosure P : intermediate document</p> <p>T : theory or principle underlying the invention E : earlier patent document, but published on, or after the filing date D : document cited in the application L : document cited for other reasons &amp; : member of the same patent family, corresponding document</p>			

EPO FORM 1503 03 92 (P/M/C01)